PRODUCT SPECIFICATION

CDTECH Model: **S070BWS117HD-DC64**

CUSTOMER Model: -

Description: **7.0 ” TFT-LCD Module with CTP**

Version: **1.0**

CDTECH	PREPARED BY	CHECKED BY	APPROVED BY
SIGNATURE			
DATE	2023.9.22	2023.9.22	2023.9.22

	SIGNATURE	DATE
CUSTOMER APPROVAL		

Record of Revisions

Version	Revise Date	Description	Page
1.0	2023.9.22	First Release	-

Contents

1. General Specifications

1.1 LCM General Information

Item	Specification	Unit
LCD Size	7.0	inch
Number of Pixels	1024 (H) RGB x 600 (V)	pixels
Display Mode	Normally Black	-
Viewing Direction	Free	o' clock
Interface	LVDS	-
Display Colors	16.7M	colors
Outline Dimension	165.00 (H) x 100.00 (V) x 7.45 (D)	mm
Active Area	154.21 (H) x 85.92 (V)	mm
Pixel Pitch	0.1505 (H) x 0.1432 (V)	mm
Driver IC	HX8282/HX8696	-
Operation Temperature	-20~70	℃
Storage Temperature	-30~80	℃

1.2 Touch Panel Information

Item	Specification
Touch Structure	G+G
Bonding Type with LCM	Perimeter Bonding
Driver IC	ILI2130
Interface	I²C
Touch Count Max	5 Points
Surface treatment	-
Surface hardness	6H
I2C slave address	0x41
Origin of coordinate	Top Left Corner

Note1:Requirements on environmental protection RoHS compliant.

2. Absolute Maximum Ratings

Item	Symbol	MIN.	MAX.	Unit	Note
Analog Supply voltage	VDD	-0.3	5.0	V	Note 1

Note 1:Permanent damage may occur to the LCD module if beyond this specification.
Functional operation should be restricted to the conditions described under normal operating conditions.

3. Elecrical Characteristics

3.1 Recommended Operating Condition for TFT LCD

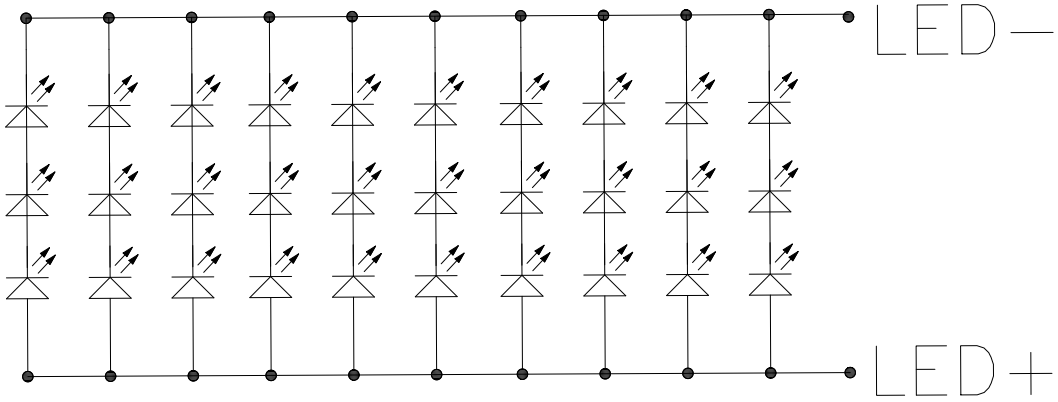
Item	Symbol	Min.	Typ.	Max.	Unit	Note
Analog Supply voltage	VDD	3.0	3.3	3.6	V	
Analog supply current	I_{VDD}	-	TBD	-	mA	VDD=3.3V
Power supply for LCD	AVDD	9.0	9.8	10.5	V	
	VGH	16.0	18.0	20	V	
	VGL	-7.5	-6.0	-5.0	V	
	VCOM	2.9	3.25	3.5	V	
Logic input voltage	VIH	0.7*VDD	-	VDD	V	
	VIL	GND	-	0.3*VDD	V	

3.2 Recommended Driving Condition for Backlight

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Driving Current	I_F	-	200	-	mA	
Driving Voltage	V_F	8.1	-	10.2	V	
Power consumption	W_{BL}	1.62	-	2.04	W	
LED Life-Time	N/A	-	50,000	-	Hours	Ta=25℃ Note 1

Note 1:LED lifetime is defined as the module brightness decay 50% of original brightness at Ta=25 degree, typical current.

Note 2:LED circuit :



3.3 Touch Panel

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Power Supply voltage	VDD	-	3.3	-	V	
Analog supply current	I_{VDD}	-	TBD	-	mA	VDD=3.3V
Input high-level voltage	VIH	0.7*VDD	-	VDD	V	
Input low -level voltage	VIL	GND	-	0.3*VDD	V	

4. Interface Pin Assignment

4.1 LCM Pin Assignment

No.	Symbol	Description
1	VCOM	Common Voltage
2	VDD	Power Voltage for digital circuit
3	VDD	Power Voltage for digital circuit
4	NC	No connection
5	RESET	Global reset pin
6	STBYB	Standby mode Normally pulled high STBYB=1,normal operation STBYB=0,timing contrller,source Driver will turn off,all output are High
7	GND	Ground
8	RXIN0-	-LVDS differential data input
9	RXIN0+	+LVDS differential data input
10	GND	Ground
11	RXIN1-	-LVDS differential data input
12	RXIN1+	+LVDS differential data input
13	GND	Ground
14	RXIN2-	-LVDS differential data input
15	RXIN2+	+LVDS differential data input
16	GND	Ground
17	RXCLKIN-	-LVDS differential clock input
18	RXCLKIN+	+LVDS differential clock input
19	GND	Ground
20	RXIN3-	-LVDS differential data input
21	RXIN3+	+LVDS differential data input
22	GND	Ground
23	NC	No connection
24	NC	No connection
25	GND	Ground
26	NC	No connection
27	DIMO	Backlight CABC controller signal output
28	SELB	6bit/8bit mode select (Note 1)
29	AVDD	Power for Analog Circuit
30	GND	Ground
31	LED-	Power for LED backlight (Cathode)
32	LED-	Power for LED backlight (Cathode)
33	L/R	Horizontal inversion (Note 2)

34	U/D	Vertical inversion	(Note 2)
35	VGL	Gate off Voltage	
36	CABCEN1	CABC H/W enable	(Note 3)
37	CABCEN0	CABC H/W enable	(Note 3)
38	VGH	Gate on Voltage	
39	LED+	Power for LED backlight (Anode)	
40	LED+	Power for LED backlight (Anode)	

Note 1: If LVDS input data is 6bit,selb must be set to high
If LVDS input data is 8bit,selb must be set to low

Note 2:When L/R=0 set right to left scan direction
When L/R=1 set left to right scan direction
When U/D=0 set top to bottom scan direction
When U/D=1 set bottom to top scan direction

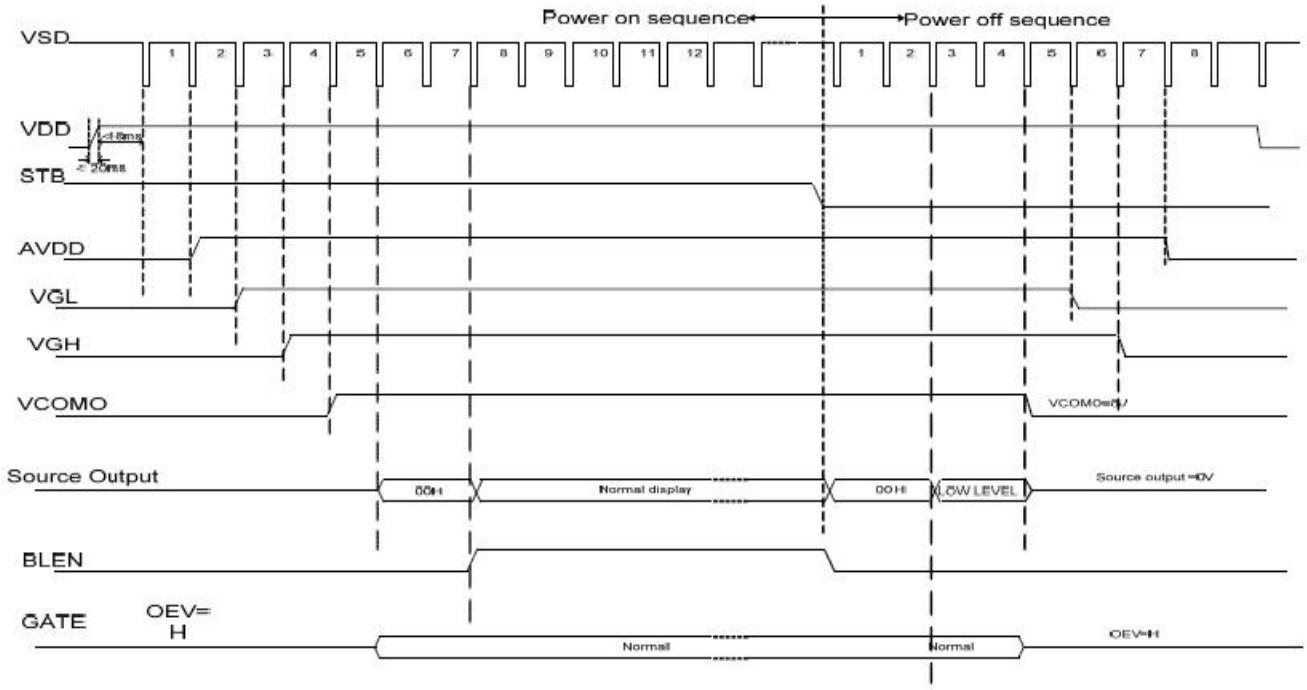
Note 3:CABC H/W enable pin. Normally pull low
When CABC_EN=”00”,CABC off.(Default mode)
When CABC_EN=”01”,user interface Image
When CABC_EN=”10”,still Picture
When CABC_EN=”11”,moving Image

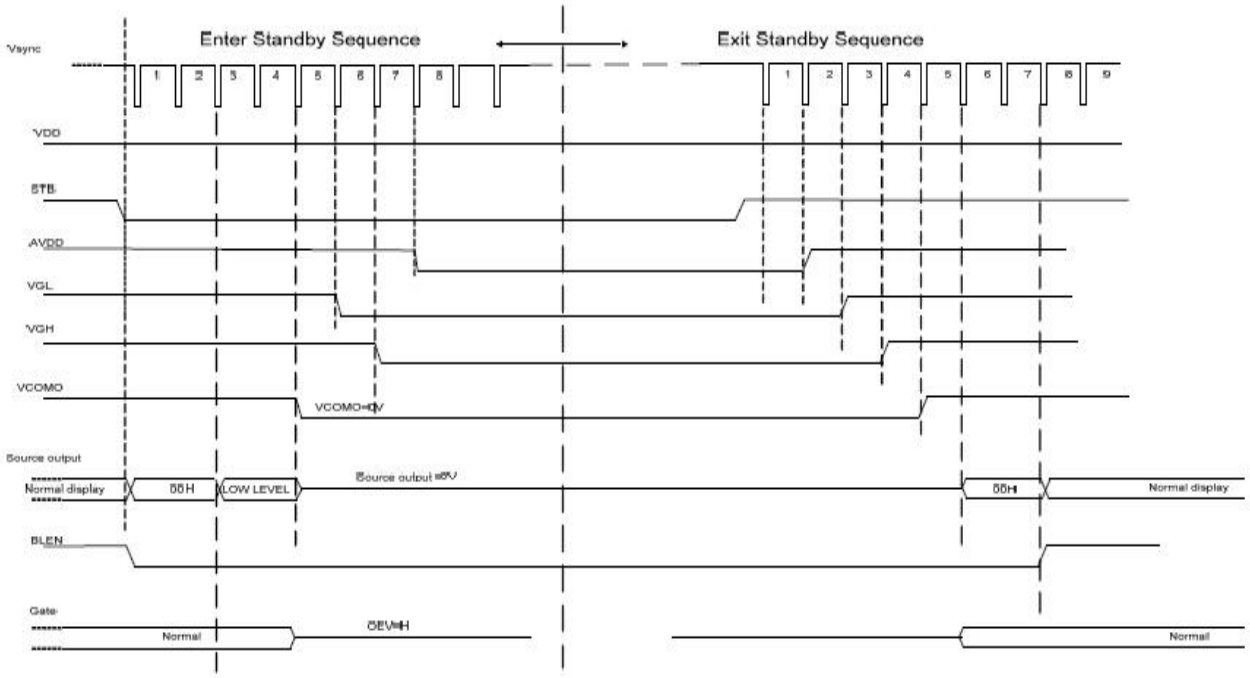
4.2 Touch FPC Pin Assignment

No.	Symbol	Description
1	SCL	I2C clock input for CTP
2	SDA	I2C data input and output for CTP
3	VDD	Power supply
4	RST	Reset Pin for CTP
5	INT	Interrupt signal for CTP
6	GND	Ground

5. Interface Characteristics

5.1 Power sequence





5.2 Timing Characteristics

5.2.1 LVDS mode DC electrical characteristics

Parameter	Symbol	Condition	Spec.			Unit
			Min.	Typ.	Max.	
Differential input high Threshold voltage	R_{XVTH}	R_{XVCM}=1.2V	-	-	+0.1	V
Differential input low threshold voltage	R_{XVTL}		-0.1	-	-	V
Input voltage range **(Singled-end)**	R_{XVIN}	-	0	-	VDD-1.2+ $\|V_{ID}\|/2$	V
Differential input common mode voltage	R_{XVCM}	-	$\|V_{ID}\|/2$	-	VDD-1.2	V
Differential input voltage	$\| V_{ID} \|$	-	0.2	-	0.6	V
Differential input leakage Current	RV_{Xliz}	-	-10	-	+10	μA
LVDS digital operating Current	Iddlvds	Fclk=65MHz, VDD=3.3V	-	15	30	mA
LVDS digital stand-by Current	Istlvds	Clock & all functions are stopped	-	10	50	μA

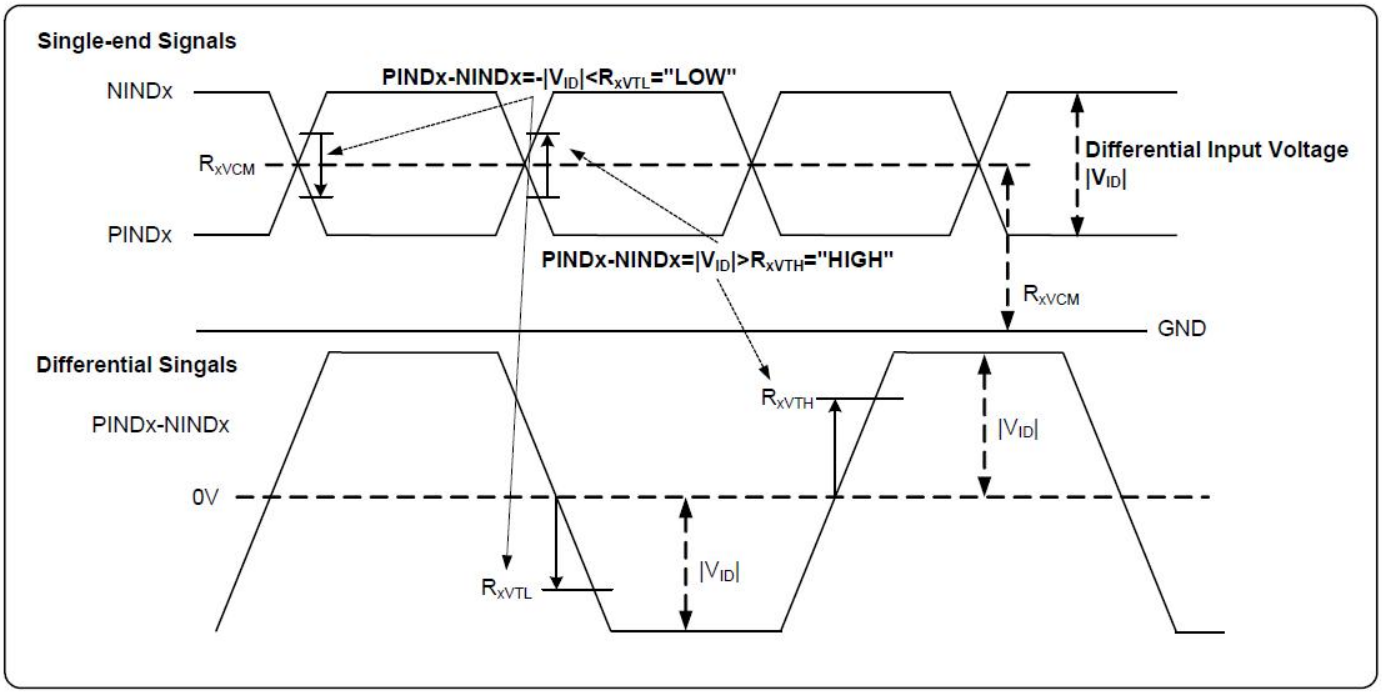
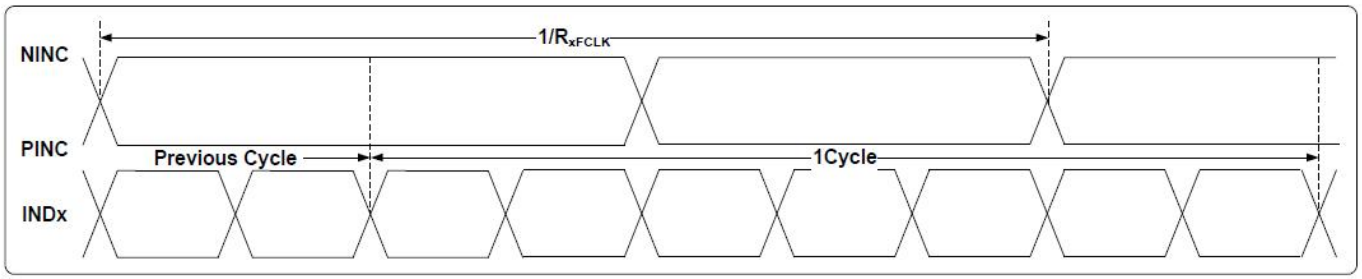
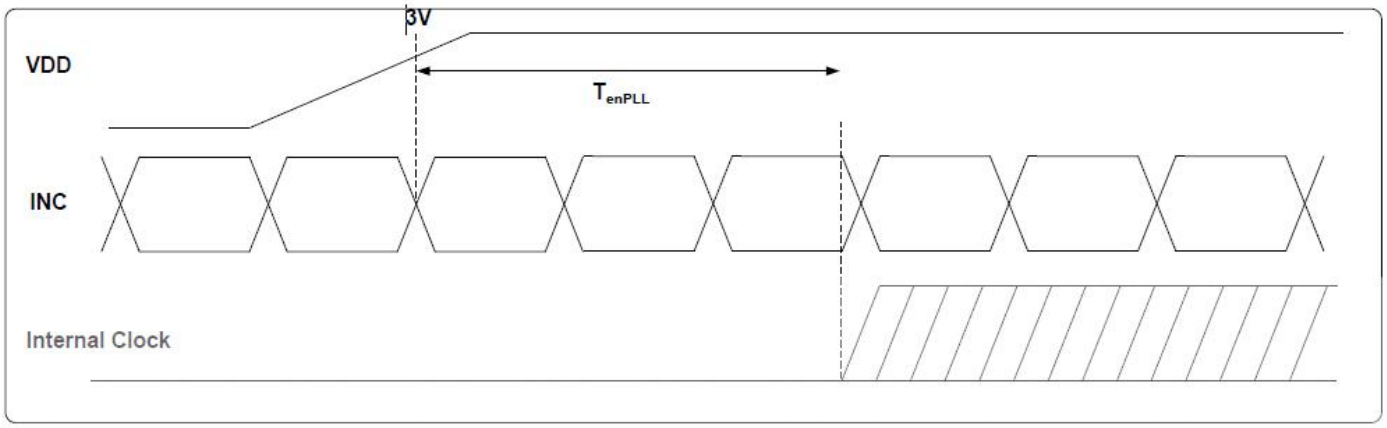


Figure5.2.1: Single-end signals

5.2.2 LVDS mode AC electrical characteristics

Parameter	Symbol	Condition	Spec.			Unit
			Min.	Typ.	Max.	
Clock frequency	R_{XFCLK}	-	20	-	71	MHz
Input data skew margin	T_{RSKM}	$\|V_{ID}\|$=400mV R_{XVCM}=1.2V R_{XFCLK}=71MHz	500	-	-	pS
Clock high time	T_{LVCH}	-	-	$4/(7xR_{XFCLK})$	-	ns
Clock low time	T_{LVCL}	-	-	$3/(7xR_{XFCLK})$	-	ns
PLL wake-up time	T_{enPLL}	-	-	-	150	μs





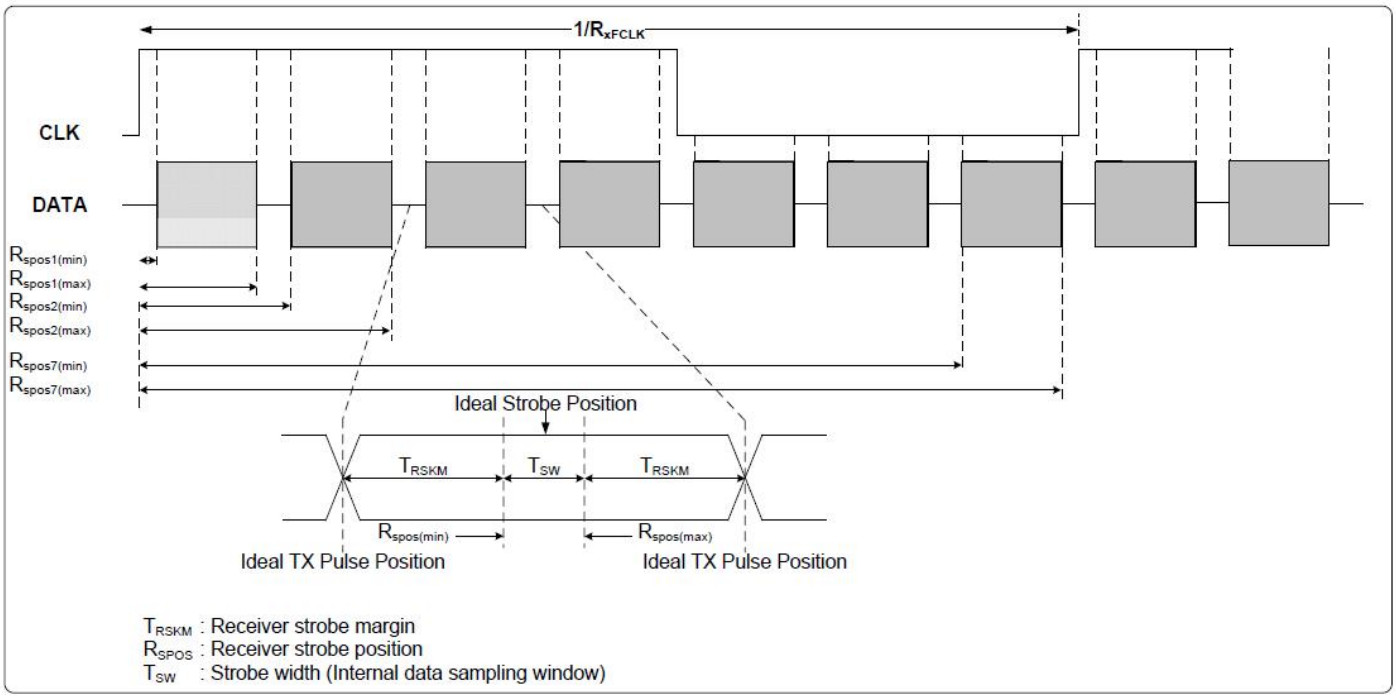


Figure 5.2.2: LVDS figure

Parameter	Symbol	Condition	Spec.			Unit
			Min.	Typ.	Max.	
Modulation frequency	SSC_{MF}	-	23	-	93	KHz
Modulation rate	SSC_{MR}	LVDS clock=71MHz center spread	-	-	±3	%

Table 5.2.2: SSC table

5.2.3 LVDS mode data input format

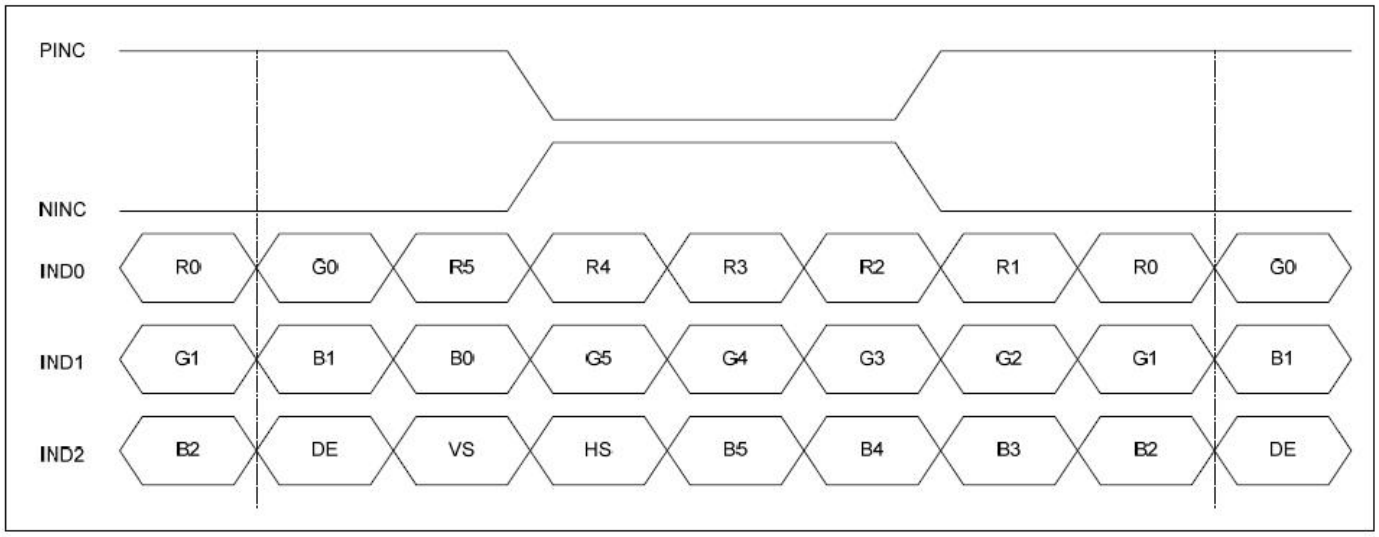


Figure 5.2.3: 6-bit LVDS input

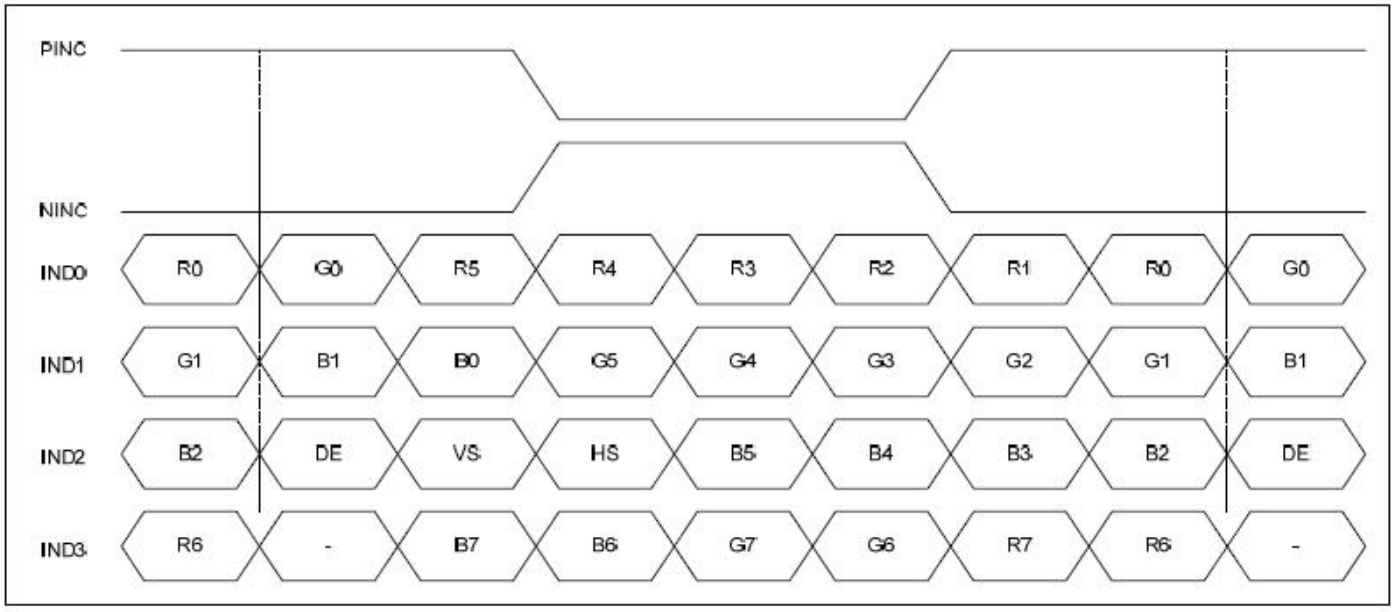


Figure 5.2.3: 8-bit LVDS input

5.2.4 Input clock and data timing diagram

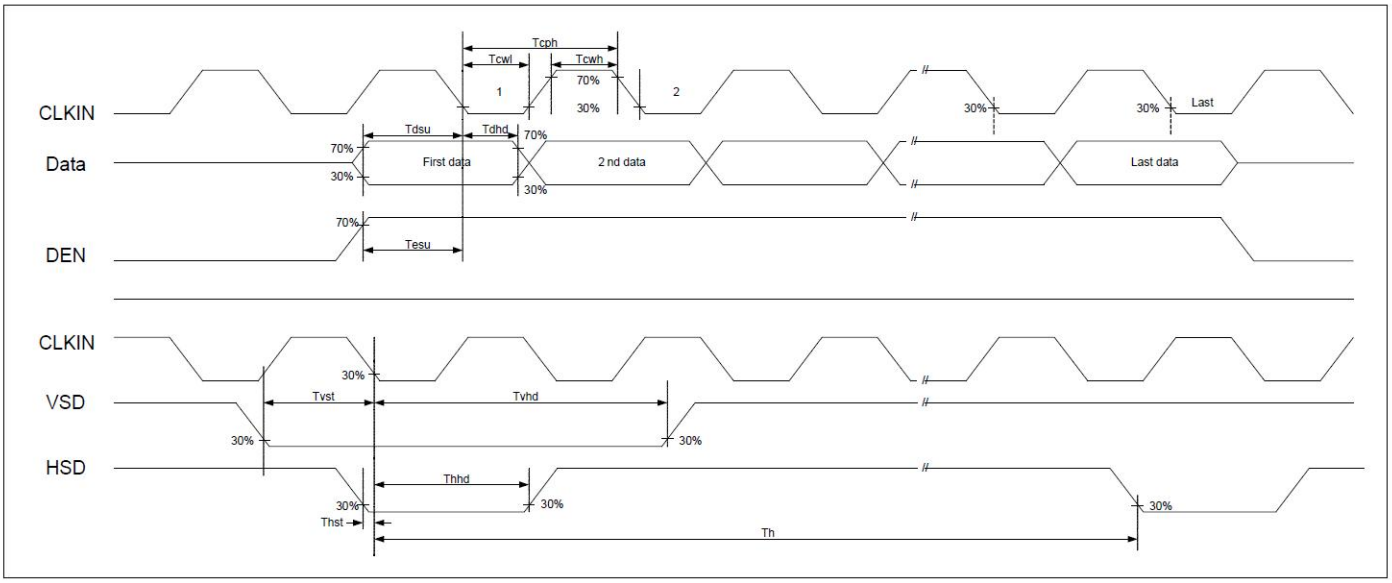


Figure 5.2.4: Input clock and data timing diagram

5.2.5 Output timing table

Parameter	Symbol	Conditions	Spec.			Unit
			Min.	Typ.	Max.	
CLKIN frequency	Fclk	VDD=3.0V~3.6V	-	65	71	MHz
CLKIN cycle time	Tclk	-	14.1	15.4	-	ns
CLKIN pulse duty	Tcwh	Tclk	40	50	60	%
Time from HSD to source output	Thso	-		64		CLKIN
Time from HSD to LD	Thld	-		64		CLKIN
Time from HSD to STV	Thstv	-		2		CLKIN
Time from HSD to CKV	Thckv	-		20		CLKIN
Time from HSD to OEV	Thoev	-		4		CLKIN
LD pulse width	Twld	-		10		CLKIN
CKV pulse width	Twckv	-		66		CLKIN
OEV pulse width	Twoev	-		74		CLKIN

Table 5.2.5: Parallel 24-bit RGB mode

5.2.6 Source output timing diagram (Cascade)

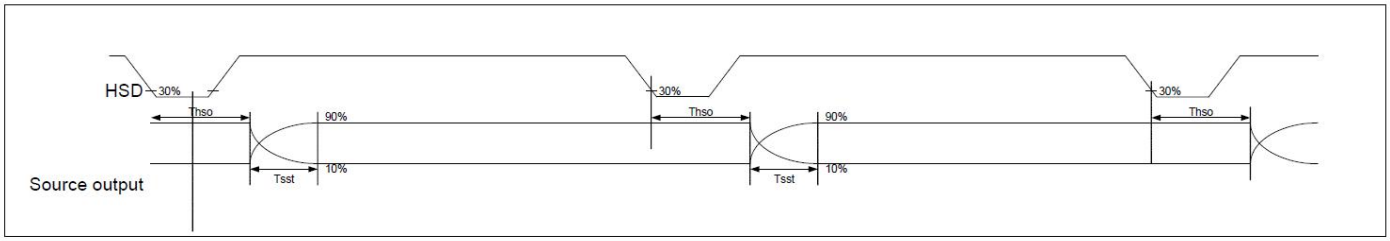


Figure 5.2.6: Source output timing diagram

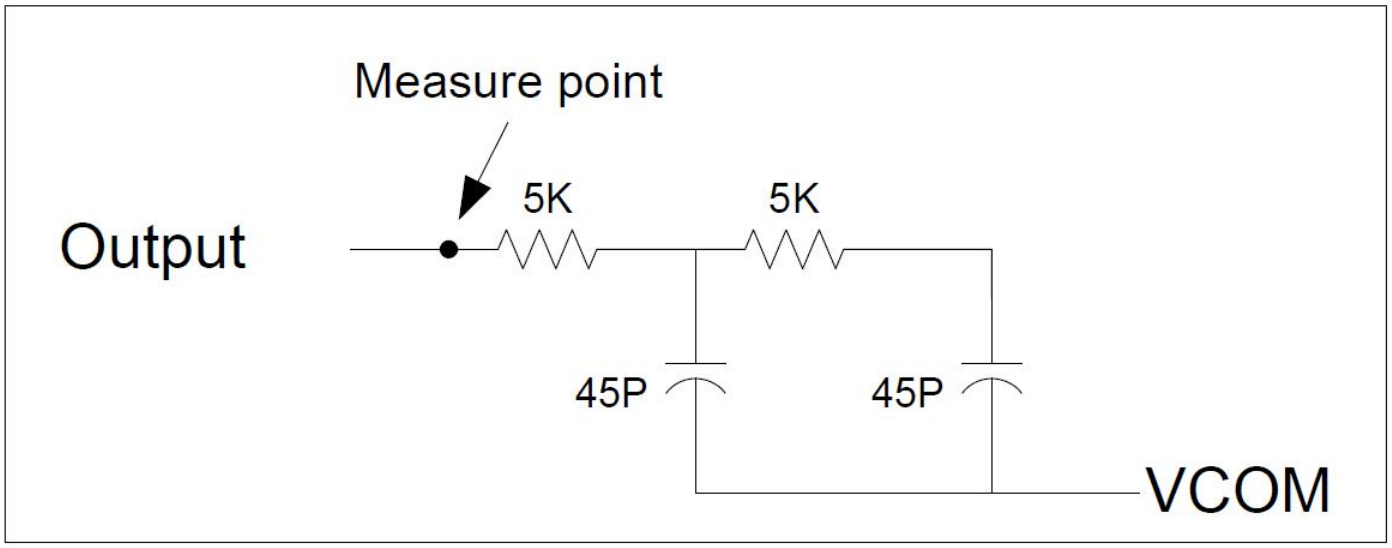


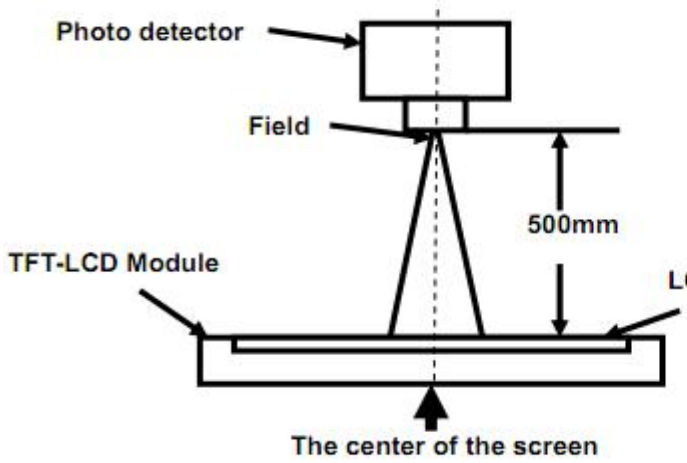
Figure 5.2.6: Output load condition

6. Optical Specifications

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Viewing Angle (CR≥10) B/L ON	θ_T	Φ=90°(12 o'clock)	70	80	-	deg	Note2
	θ_B	Φ=270°(6 o'clock)	70	80	-	deg	Note2
	θ_L	Φ=180°(9 o'clock)	70	80	-	deg	Note2
	θ_R	Φ=0°(3 o'clock)	70	80	-	deg	Note2
Response Time	T_{ON}	Normal θ=Φ=0°	-	15	20	msec	Note4
	T_{OFF}		-	15	20	msec	Note4
Contrast Ratio	CR		500	800	-	-	Note1 Note3
Color Chromaticity	W_X		0.258	0.308	0.358	-	Note1 Note5
	W_Y		0.286	0.336	0.386	-	Note1 Note5
Luminance	L		350	400	-	cd/m²	Note1 Note7
Luminance Uniformity	Y_U		75	80	-	%	Note1 Note6
NTSC	-		-	50	-	%	-

Note 1:Definition of optical measurement system

The optical characteristics should be measured in dark room. After 5 minutes operation, the optical properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.



Item	Photo detector	Field
Contrast Ratio	SR-3A	1°
Luminance		
Chromaticity		
Lum Uniformity		
Response Time	BM-7A	2°

Note 2:Definition of viewing angle range and measurement system
Viewing angle is measured at the center point of the LCD by CONOSCOPE(ergo-80).

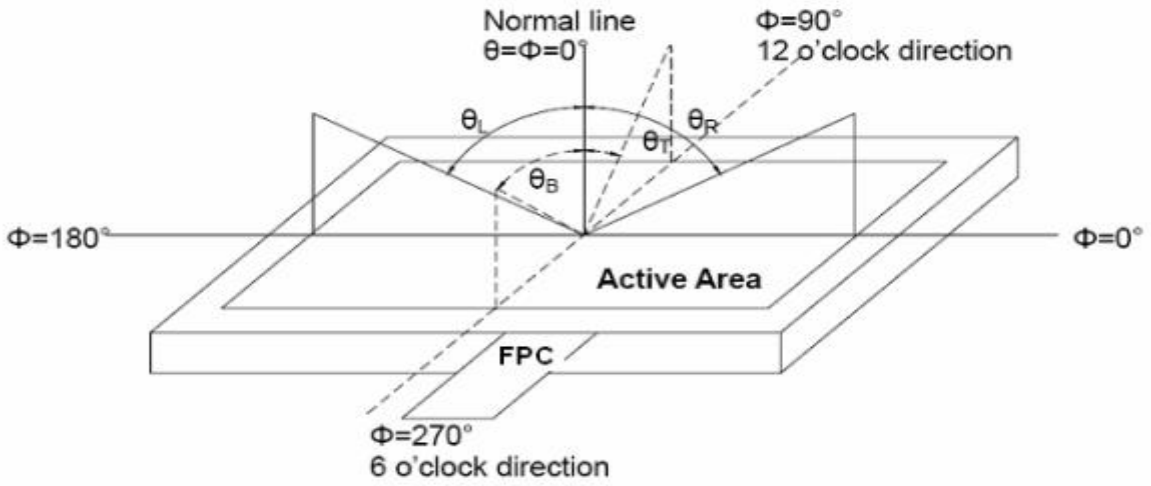


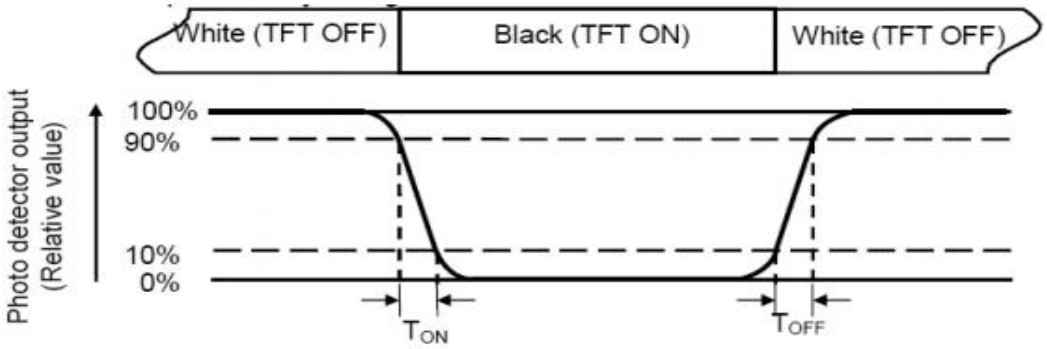
Fig. 1 Definition of viewing angle

Note 3: Definition of contrast ratio

$$CR = \frac{\text{Luminance with all pixels white}}{\text{Luminance with all pixels black}}$$

Note 4: Definition of Response time

The response time is defined as the LCD optical switching time interval between “White” state and “Black”state. Rise time (TON) is the time between photo detector output intensity changed from 90% to 10%. And fall time (TOFF) is the time between photo detector output intensity changed from 10% to 90%.



Note 5:Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

Note 6:Definition of Luminance Uniformity

The luminance uniformity in surface luminance is determined by measuring luminance at each test position 1 through n, and then dividing the maximum luminance of n points luminance by minimum luminance of n points luminance. For more information see FIG.2.

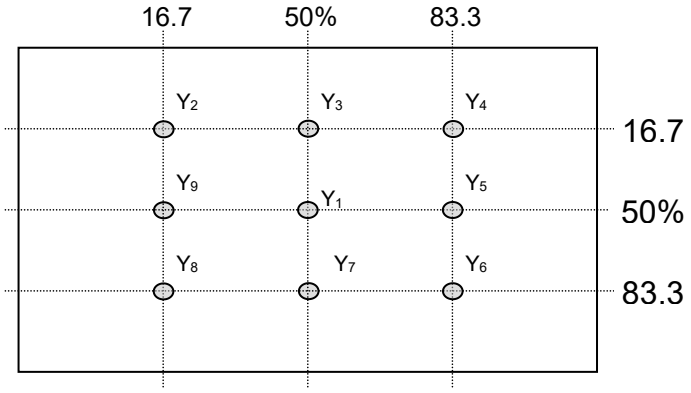


Fig. 2 Definition of points

Note 7:Definition of Luminance (Refer Fig. 2)

Surface luminance is the luminance with all pixels displaying white.

Lv = Average Surface Luminance with all white pixels(P1,P2,P3, ...,Pn).

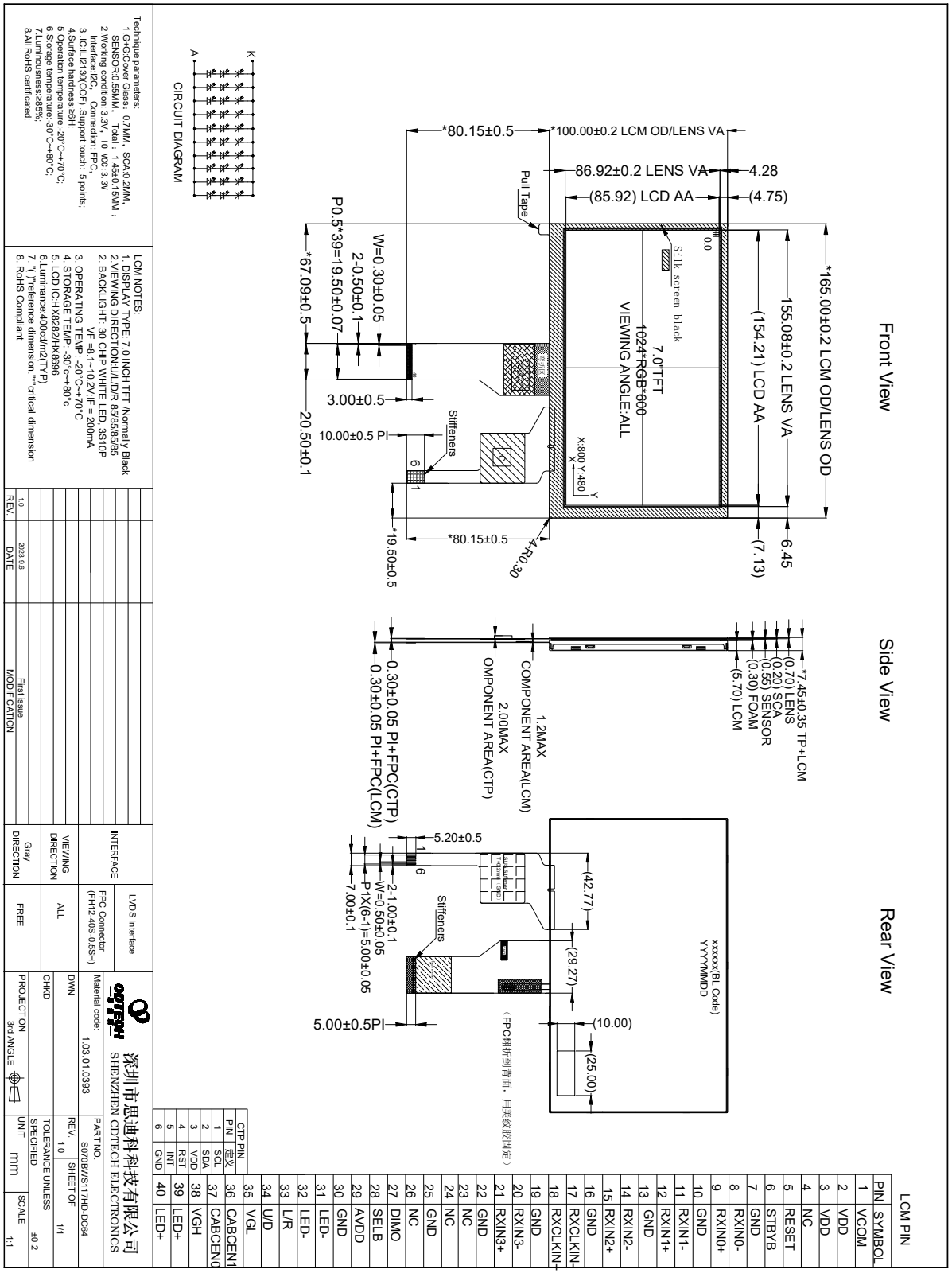
7. Reliability Test Items

Test Item	Test Conditions	
High Temperature Storage	Ta= +80℃	96hrs
Low Temperature Storage	Ta= -30℃	96hrs
High Temperature Operation	Ta= +70℃	96hrs
Low Temperature Operation	Ta= -20℃	96hrs
High Temperature and Humidity Storage	Ta= +60℃, 90% RH	96hrs
Thermal Shock (Non-operation)	-30℃/30 min ~ +80℃/30 min for 20 cycles Start with cold temperature end with high temperature	
Electro Static Discharge	Contact = ± 4 kV, class B Air = ± 8 kV, class B R=330Ω,C=150pF	
Vibration	Sweep: 10Hz~55Hz~10Hz Stroke: 1.5mm 2 hrs for each direction of X .Y. Z.	
Mechanical Shock	60G 6ms,±X,±Y,±Z 3 times for each direction	
Package Drop Test	Height: 60 cm 1 corner, 3 edges, 6 surfaces	

Notes: The test result shall be evaluated after the sample has been left at room temperature and humidity for 2 hours without load. No condensation shall be accepted. The sample will not be accepted if appear these defects:

1). Air bubble in the LCD
2). Seal leak or Glass crack
3). Non display or abnormal display
4). Brightness reduction >50%

8. Mechanical Drawing



LCM PIN		
PIN	SYMBOL	
1	VCOM	
2	VDD	
3	VDD	
4	NC	
5	RESET	
6	STBYB	
7	GND	
8	RXIN0-	
9	RXIN0+	
10	GND	
11	RXIN1-	
12	RXIN1+	
13	GND	
14	RXIN2-	
15	RXIN2+	
16	GND	
17	RXCLKIN-	
18	RXCLKIN+	
19	GND	
20	RXIN3-	
21	RXIN3+	
22	GND	
23	NC	
24	NC	
25	GND	
26	NC	
27	DIMO	
28	SELB	
29	AVDD	
30	GND	
31	LED-	
32	LED-	
33	L/R	
34	U/D	
35	VGL	
36	CABCEN1	
37	CABCEN0	
38	VGH	
39	LED+	
40	LED+	

CTP PIN	
PIN	定义
1	SCL
2	SDA
3	VDD
4	RST
5	INT
6	GND

Technique parameters:
1.G+G:Cover Glass：0.7MM，SCA:0.2MM，SENSOR:0.55MM，Total：1.45±0.15MM；
2.Working condition: 3.3V，10 VDC; 3.3V
3.Interface:FPC Connection; FPC；
4.IC:HL12130(COF); Support touch: 5 points;
5.Surface hardness:6H;
6.Operation temperature:-20°C~+70°C;
7.Storage temperature:-30°C~+80°C;
8.Luminous:≥85%;
9.All RoHS certificated;

LCM NOTES:
1. DISPLAY TYPE: 7.0 INCH TFT /Normally Black
2. VIEWING DIRECTION:U/L/D/R 85/85/85/85
3. BACKLIGHT: 30 CHIP WHITE LED, 3S*10P
VF =8.1~10.2V,IF = 200mA
4. OPERATING TEMP: -20°C~+70°C
5. STORAGE TEMP: -30°c~+80°c
6. LCD IC:HX8282/HX8696
7. Luminance:400cd/m2(TYP)
8. "( )":reference dimension, "*":critical dimension
9. RoHS Compliant

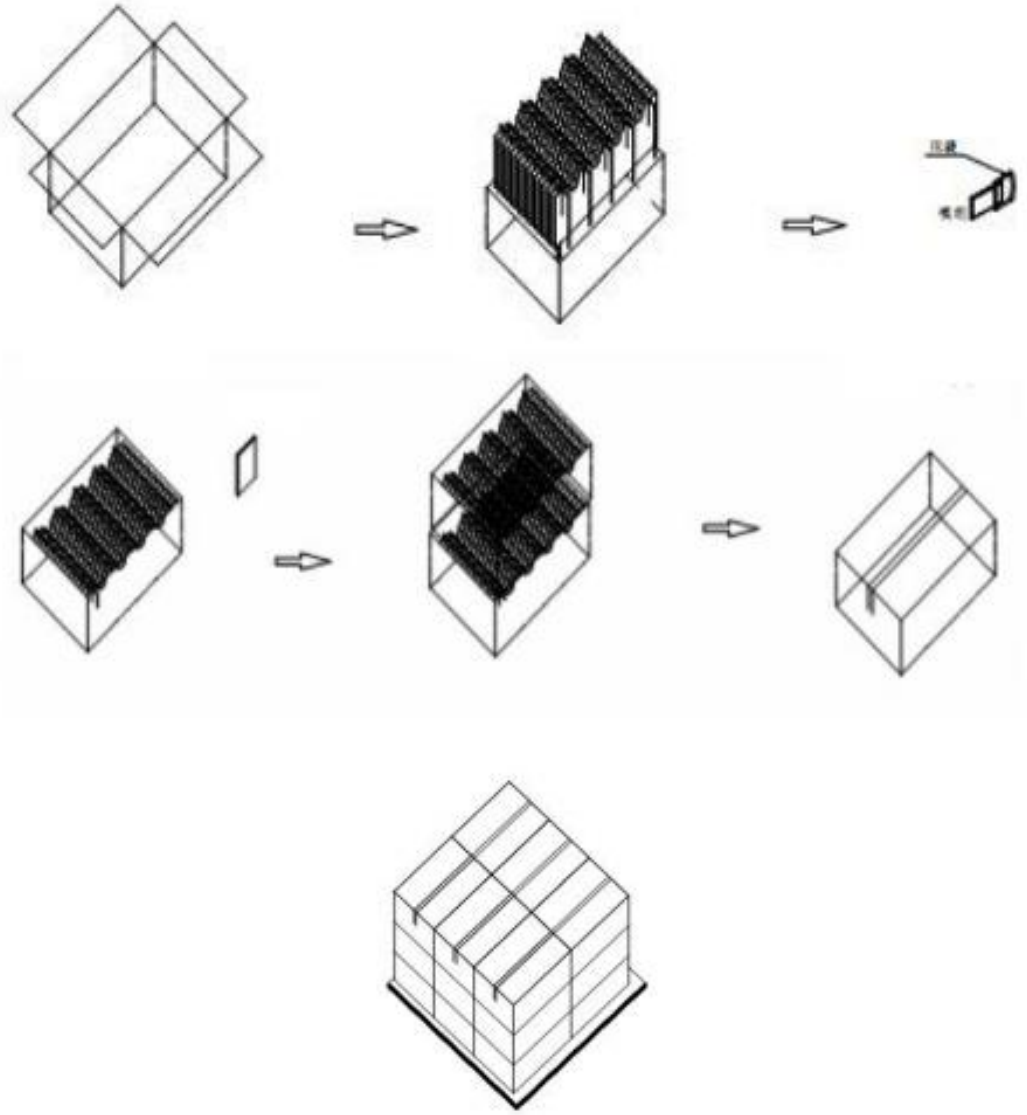
REV.	DATE	MODIFICATION			
1.0	2023.9.6	First issue	INTERFACE	LVDS Interface	
			VIEWING DIRECTION	FPC Connector (FH12-40S-0.5SH)	
			Gray DIRECTION	ALL	
				FREE	

深圳市思迪科科技有限公司
SHENZHEN CDTECH ELECTRONICS

Material code:	1.03.01.0393	PART NO.	S070BWS117HD-DC64
DWN		REV. 1.0	SHEET OF 1/1
CHKD		TOLERANCE UNLESS SPECIFIED	±0.2
PROJECTION 3rd ANGLE		UNIT mm	SCALE 1:1

9. Packing

Packing Method



Steps:

1. Put module into tray cavity:
2. Tray stacking
3. Put 1 cardboard under the tray stack and 1 cardboard above:
4. Fix the cardboard to the tray stack with adhesive tape:
5. Put the tray stack into carton.
6. Carton sealing with adhesive tape

10. Precautions for Use of LCD modules

10.1 Handling Precautions

10.1.1. The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

10.1.2. If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

10.1.3. Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

10.1.4. The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

10.1.5. If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

- Isopropyl alcohol
- Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketene
- Aromatic solvents

10.1.6. Do not attempt to disassemble the LCD Module.

10.1.7. If the logic circuit power is off, do not apply the input signals.

10.1.8. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

10.1.8.1. Be sure to ground the body when handling the LCD Modules.

10.1.8.2. Tools required for assembly, such as soldering irons, must be properly ground.

10.1.8.3. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

10.1.8.4. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

10.2 Storage Precautions

10.2.1.When storing the LCD modules,avoid exposure to direct sunlight or to the light of fluorescent lamps.

10.2.2.The LCD modules should be stored under the storage temperature range if the LCD modules will be stored for a long time,the recommend condition is :

Temperature : 0℃ ~40℃ Relatively humidity: ≤80%

10.2.3.The LCD modules should be stored in the room without acid, alkali and harmful gas.

10.3 Transportation Precautions

The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.