DATA SHEET

(DOC No. HX8571-E27ADBG-X-DS)

HX8571-E27ADBG-X

Touch Screen Controller

Version 01 June, 2017

Himax Technologies, Inc.
http://www.himax.com.tw

Revision History

June, 2017

Version	Date	Description of changes
01	2017/06/19	New setup.
	2017/7/6	Page 12 1. Remove the Min. of Rise time of both SDA and SCL signals. 2. Remove the Min. of fall time of both SDA and SCL signals.
	2017/12/26	Page 7 1. Both SDA and SCL damping resistance change 150 Ohms.

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1. General Description

HX8571-E27ADBG-X is a capacitive touch screen control IC designed for I^2C interface that supports touch screen of 27 sensing channels I/O. HX8571-E27ADBG-X also provide flexibility and selective functionality solution for the variety application of touch screen.

HX8571-E27ADBG-X supports the flash memory to store calibration data and application parameters. HX8571-E27ADBG-X is suitable for any capacitive touch screen application combined with high performance controller and friendly user interfaces, and wide operating temperature range.

2. Features

- Projective touch screen control IC
- Programmable scan line
- Support 27 channels I/O
- Both self capacitance and mutual capacitance technology adopted
  - HX8571-E27ADBG-X for multi touch **(all points)**
- Low power consumption
- Auto noise filter function
- Support I^2C interface
- Customer function design **(embedded with RDC8051)**
  - 256 byte scratchpad RAM interface
  - 12K byte external RAM
  - 32K flash program memory address space
- 2.7V to 3.6V operating voltage
  - VCCA(**Analog power**): 2.7V to 3.6V
  - VCCD(**Digital power**): 1.65V to 3.6V
- Temperature range: -40 ~ +85 ℃
- Flash memory to store calibration data and application parameters

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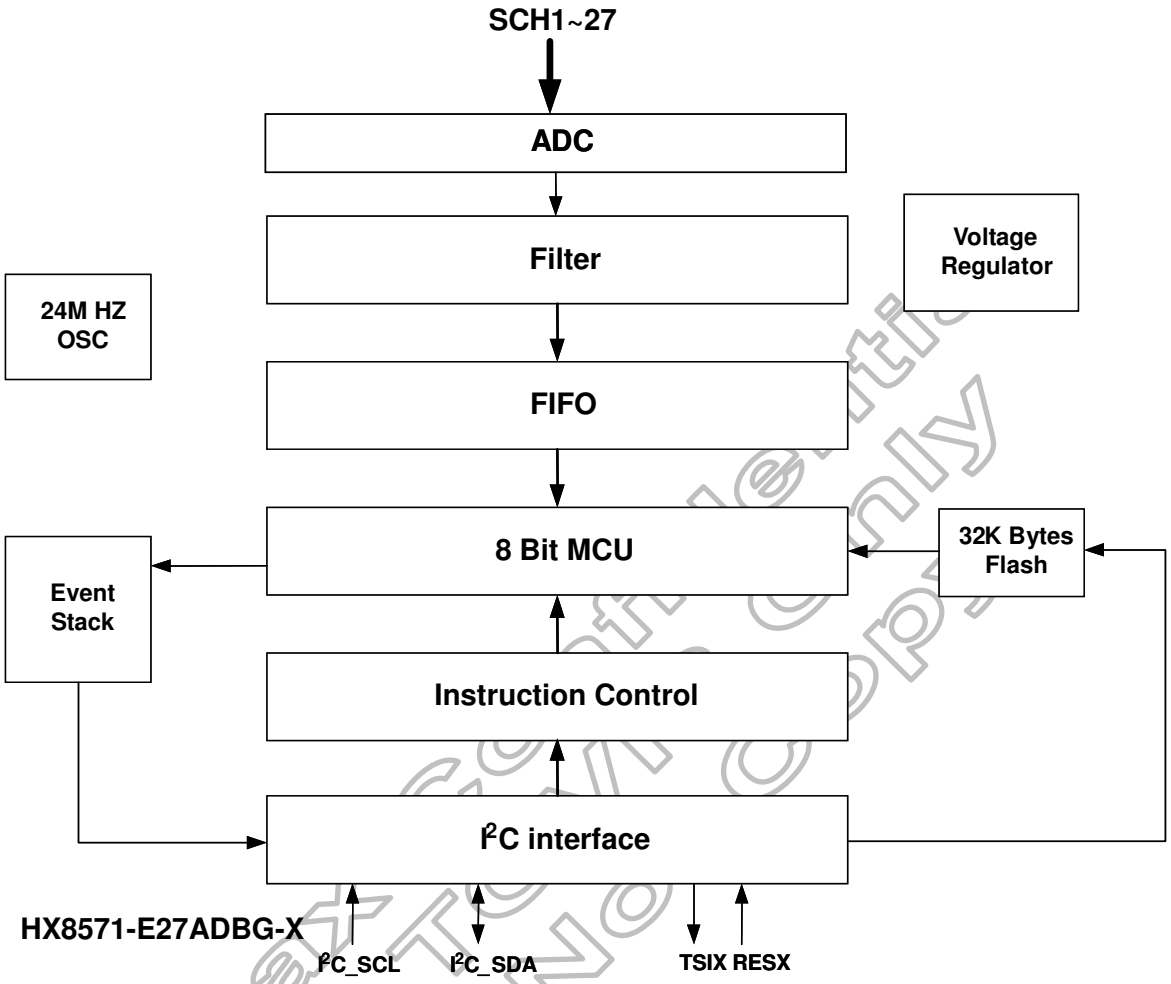
3. System Diagram



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4. Pin Description

4.1 HX8571-E27ADBG-X 40-pin QFN

No.	Type	Name	Description
1	In/Out	SCH1	Touch screen channel **(SCH1)**.
2	In/Out	SCH2	Touch screen channel **(SCH2)**.
3	In/Out	SCH3	Touch screen channel **(SCH3)**.
4	In/Out	SCH4	Touch screen channel **(SCH4)**.
5	In/Out	SCH5	Touch screen channel **(SCH5)**.
6	In/Out	SCH6	Touch screen channel **(SCH6)**.
7	In/Out	SCH7	Touch screen channel **(SCH7)**.
8	In/Out	SCH8	Touch screen channel **(SCH8)**.
9	In/Out	SCH9	Touch screen channel **(SCH9)**.
10	In/Out	SCH10	Touch screen channel **(SCH10)**.
11	In/Out	SCH11	Touch screen channel **(SCH11)**.
12	In/Out	SCH12	Touch screen channel **(SCH12)**.
13	In/Out	SCH13	Touch screen channel **(SCH13)**.
14	In/Out	SCH14	Touch screen channel **(SCH14)**.
15	Power	VSS	Ground connection.
16	In/Out	SCH15	Touch screen channel **(SCH15)**.
17	In/Out	SCH16	Touch screen channel **(SCH16)**.
18	In/Out	SCH17	Touch screen channel **(SCH17)**.
19	In/Out	SCH18	Touch screen channel **(SCH18)**.
20	In/Out	SCH19	Touch screen channel **(SCH19)**.
21	Power	VSS	Ground connection.
22	In/Out	SCH20	Touch screen channel **(SCH20)**.
23	In/Out	SCH21	Touch screen channel **(SCH21)**.
24	In	XRES	Active low external reset.
25	Out	TSIX	Touch Screen Interrupt. Touch screen Interrupt line; Interrupt active when the line is low.
26	Output	TEST_RX	Test pin. It is for internal test or GPIO pin used. Let it open if not used.
27	IO	I^2C_SDA(1)	Data line for I^2C interface. Connect the optional damping resistance 150 Ohms if I^2C trace length on main board is approximate or larger than 100mm.
28	In	I^2C_SCL(1)	Serial clock lines for I^2C interface. Connect the optional damping resistance 150 Ohms if I^2C trace length on main board is approximate or larger than 100mm.
29	Power	VCCD	Digital power supply voltage. The range is 1.65V ~ 3.6V. Connect the optional stabilizing capacitor 1µF.
30	Power	VSS	Ground connection.
31	Power	VCCA	Analog power supply voltage. The range is 2.7V ~ 3.6V. Connect the optional stabilizing capacitor 1µF.
32	Power	CVDD	Connect the optional stabilizing capacitor 1µF.
33	Power	VCIOUT	Connect the optional stabilizing capacitor 0.1µF.
34	In/Out	SCH22	Touch screen channel **(SCH22)**.
35	In/Out	SCH23	Touch screen channel **(SCH23)**.
36	In/Out	SCH24	Touch screen channel **(SCH24)**.
37	Power	VSS	Ground connection.
38	In/Out	SCH25	Touch screen channel **(SCH25)**.
39	In/Out	SCH26	Touch screen channel **(SCH26)**.
40	In/Out	SCH27	Touch screen channel **(SCH27)**.

Note: (1) The HX8571-E27ADBG-X QFN only support I^2C interface and the slave address is 90/94h.

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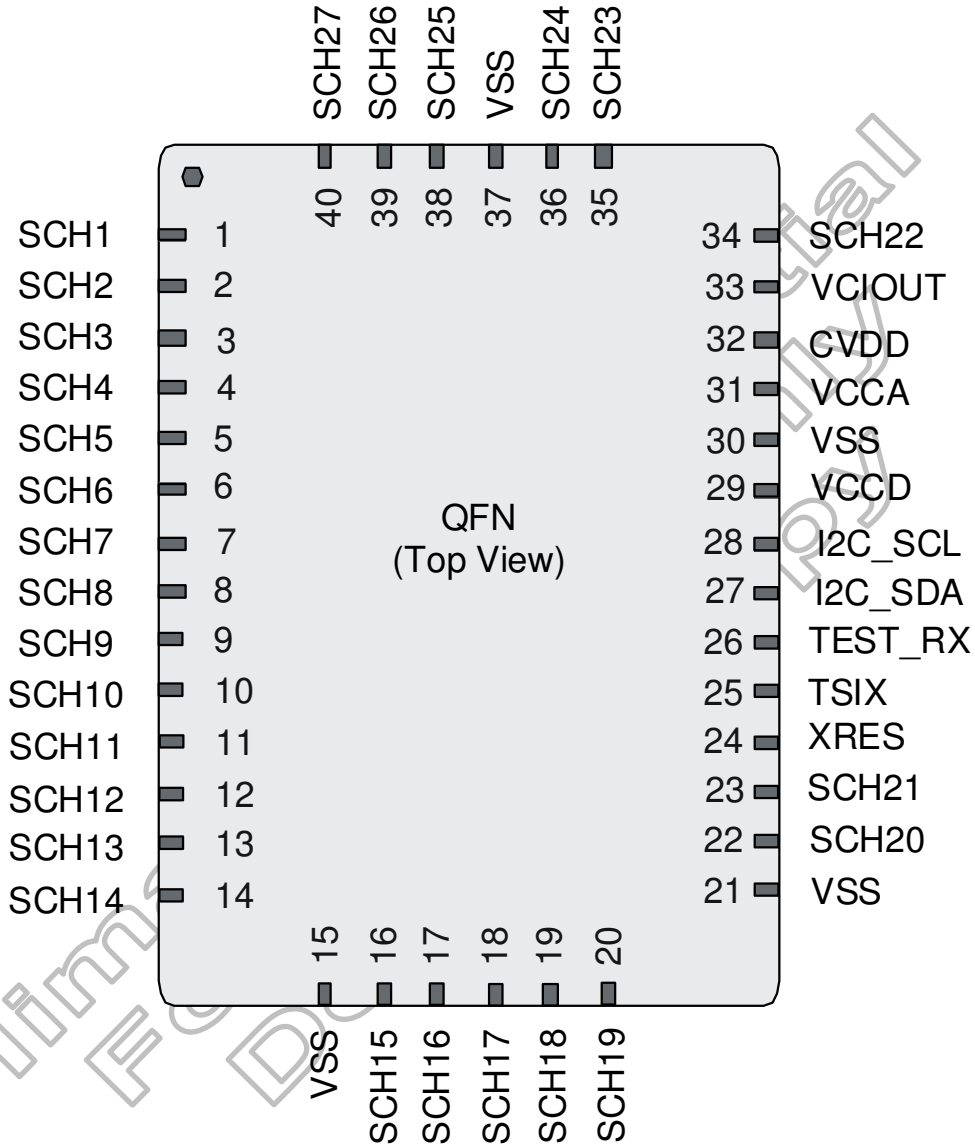
5. Pin Assignment

5.1 HX8571-E27ADBG-X 40-pin QFN



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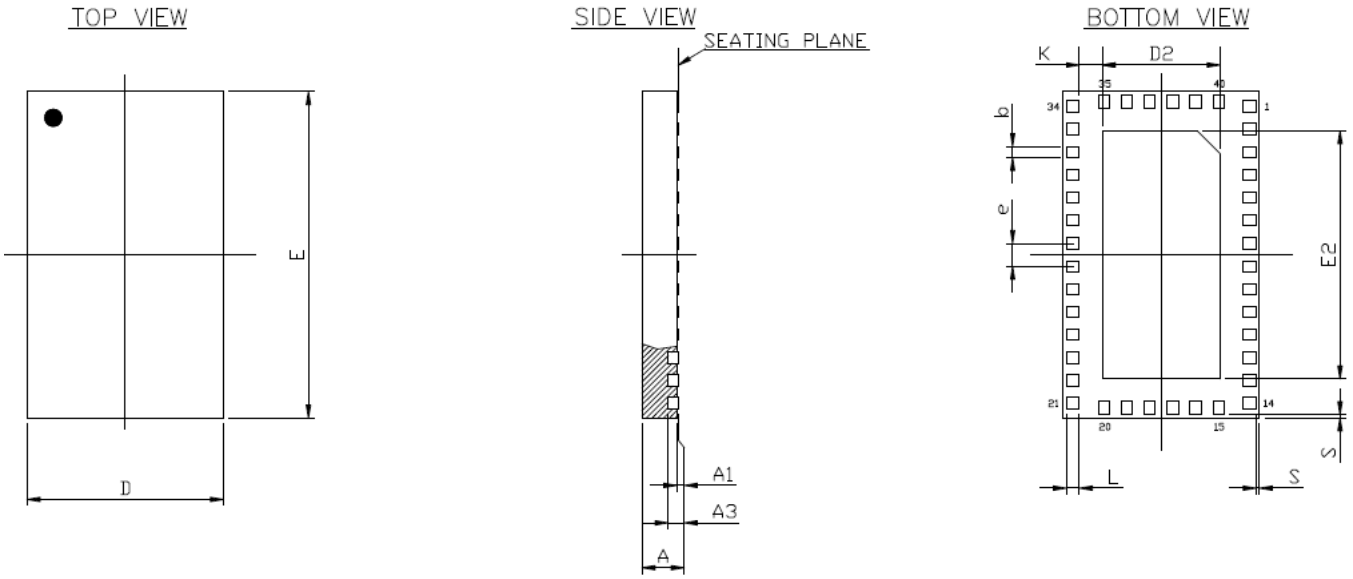
6. Package Information

6.1 HX8571-E27ADBG-X 40-pin QFN (3mm×5mm×0.55mm)



SYMBOLS	MIN.	NOM.	MAX.
A	0.50	0.55	0.60
A1	0.00	0.02	0.05
A3		0.150 REF.	
b	0.14	0.17	0.20
D	2.90	3.00	3.10
E	4.90	5.00	5.10
e		0.35 BSC	
L	0.15	0.20	0.25
K	0.20	—	—
S	0.00	0.05	0.10

Exposed Pad Size				
L/F	D2		E2	
	MIN	MAX	MIN	MAX
①	1.60	2.00	3.60	4.00

NOTES :

1. ALL DIMENSIONS ARE IN MILLIMETERS.
2. DIMENSION b APPLIES TO METALLIZED TERMINAL AND IS MEASURED BETWEEN 0.15mm AND 0.30mm FROM THE TERMINAL TIP. IF THE TERMINAL HAS THE OPTIONAL RADIUS ON THE OTHER END OF THE TERMINAL, THE DIMENSION b SHOULD NOT BE MEASURED IN THAT RADIUS AREA.
3. BILATERAL COPLANARITY ZONE APPLIES TO THE EXPOSED HEAT SINK SLUG AS WELL AS THE TERMINALS.

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7. Absolute Maximum Ratings

Parameter	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
Supply voltage	V_{IN}	-0.3	-	7	V
Switch control signals output current	Output current	-	50	-	mA
Enable control voltage range	Logic Input	-0.3	-	V_{IN}+0.3	V
Output control driver	Output voltage	-0.3	-	V_{IN}	V

8. Thermal Information

Parameter	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
Operating junction temperature	T_J	-40	-	125	℃
Operating temperature range **(Ambient)**	T_{OP}	-40	-	85	℃
Storage temperature range	T_{STG}	-55	-	150	℃
Lead soldering temperature, 10 seconds	-	-	-	260	℃
Power dissipation @ T_A=25℃ **(no load, fop=100kHz)**	P_D	-	-	50	mW
Thermal resistance	θ_{JA}	-	-	60	℃/W

9. ESD Rating

Parameter	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
Human body model	ESD	-	2	-	KV
Machine model		-	200	-	V

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10. Electrical Specifications

10.1 DC characteristics

(VCCD=1.65 ~ 3.6V, VCCA=2.7 ~ 3.6V, T_A=-40 ~ 85 °C)

Parameter	Symbol	Condition	Spec.			Unit
			Min.	Typ.	Max.	
Input high voltage	V_{IH}	VCCD=1.65 ~ 3.6V VCCA=2.7 ~ 3.6V	0.75VCCD	-	VCCD	V
Input low voltage	V_{IL}		0	-	0.25VCCD	V
Output low voltage **(SDO)**	V_{OL1}	VCCD=1.65 ~ 3.6V I_{OL}=1.0mA	0	-	0.2VCCD	V
Current consumption deep-standby mode **(VCCA/VCCD-VSS)**	$I_{ST(VDD)}$	VCCA=3.3V, VCCD=3.3V, T_A=25°C	-	20	-	µA
Current consumption active mode **(VCCA/VCCD-VSS)**	$I_{ACTIVE(VDD)}$	VCCA=3.3V, VCCD=3.3V, T_A=25°C	-	Note(1)	-	mA
Current consumption idle mode **(VCCA/VCCD-VSS)**	$I_{ACTIVE(VDD)}$	VCCA=3.3V, VCCD=3.3V, T_A=25°C	-	Note(2)	-	mA
Minimum slew rate of VCCA and VCCD	-	-	1	-	-	V/ms
Ripple voltage of VCCD and VCCA	V_R	VCCA=3.3V, VCCD=3.3V, T_A=25°C	-	-	100	mVp-p

Note: (1) Depend on report rate setting, the range is 7 ~ 10mA.
(2) Depend on report rate setting, the range is 0.6 ~ 0.9mA.

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10.2 AC characteristics of the SDA and SCL bus lines for I²C-bus devices

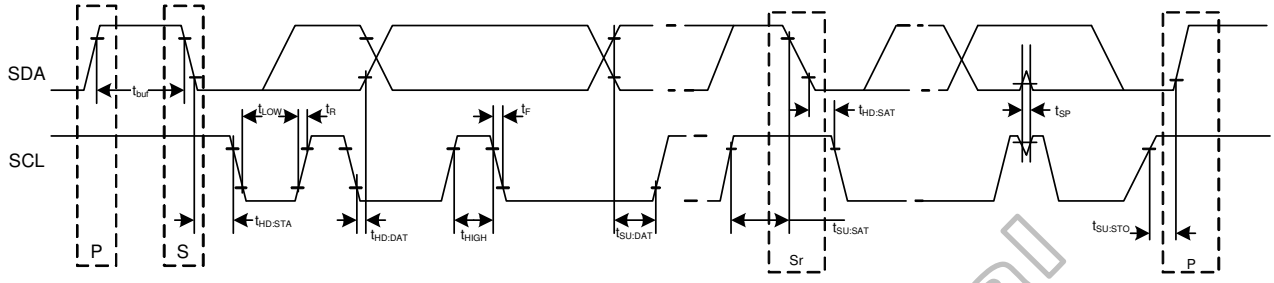

Figure 10.1: I²C timing

Parameter	Symbol	Standard-mode I²C-bus		Fast-mode I²C-bus		Unit
		Min.	Max.	Min.	Max.	
SCL clock frequency	f_{SCL}	0	100	0	400	KHz
Bus free time between STOP and START condition	t_{BUF}	4.7	-	1.3	-	µs
Hold time **(repeated)** START condition After this period, the first clock pulse is generated	$t_{HD:STA}$	4.0	-	0.6	-	µs
Low period of the SCL clock (Note1)	t_{LOW}	4.7	-	1.3	-	µs
High period of the SCL clock (Note1)	t_{HIGH}	4.0	-	0.6	-	µs
Setup time for a repeated START condition (Note2)	$t_{SU:STA}$	4.7	-	0.6	-	µs
Data hold time (Note3)	$t_{HD:DAT}$	0	-	0	0.9	µs
Data setup time (Note4)	$t_{SU:DAT}$	250	-	100	-	ns
Rise time of both SDA and SCL signals (Note6)	t_R	-	1000	-	300	ns
Fall time of both SDA and SCL signals (Note6)	t_F	-	300	-	300	ns
Setup time for STOP condition	$t_{SU:STO}$	4.0	-	0.6	-	µs
Capacitive load for each bus line (Note5)	C_b	-	400	-	400	pF

Note: (1) All values are referred to VIH **(0.75VCCD)** and VIL **(0.25VCCD)** level.

(2) A device must internally provide a hold time of at least 300ns for the SDA signal **(referred to the VIH of the SCL signal)** in order to bridge the undefined region of the falling edge of SCL.

(3) The maximum $t_{HD:DAT}$ has only to be met if the device does not stretch the low period (t_{LOW}) of the SCL signal.

(4) A fast-mode I²C-bus device can be used in a standard-mode I²C-bus system, but the requirement $t_{SU:DAT} \geqq$ 250ns must then be met. This will automatically be the case if the device does not stretch the low period of the SCL signal. If such a device does stretch the low period of the SCL signal, it must output the next data bit to the SDA line $t_{R\,max} + t_{SU:DAT}$ = 1000+250=1250ns **(according to the standard-mode I²C-bus specification)** before the SCL line is released.

(5) C_b = total capacitance of one bus line in pF.

(6) If a spark or noise appear on SDA line and keep more than 25ns, START or STOP condition will be identified if SCL line keep high at this time.

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11. Interface

11.1 System interface

The HX8571-E27ADBG-X supports I^{2}C interface **(NXP(x-Philips) serial interface)**.

11.2 Transfer protocol (I^{2}C interface)

HX8571-E27ADBG-X support I^{2}C interface that need 2 hardware pin – serial data **(SDA)** and serial clock **(SCL)**, carry information between the devices connected to the bus. The I^{2}C bus supports serial, 8-bit oriented, bi-directional data transferred at a rate up to 100Kbit/s in the standard-mode, or up to 400Kbit/s in the fast-mode.

The data on the SDA line must be stable during the high period of the clock. The high or low state of the data line can only change when the clock signal on the SCL line is low.

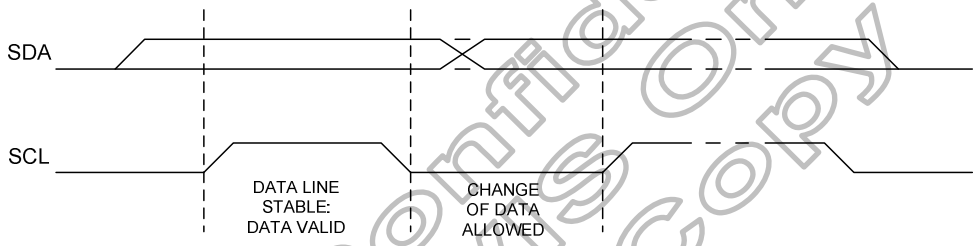

Figure 11.1: I^{2}C signal timing

Within the procedure of the I^{2}C-bus, unique situations arise which are defined as START and STOP conditions. A high to low transition on the SDA line while SCL is high is one such unique case. This situation indicates a START condition. A low to high transition on the SDA line while SCL is high defines a STOP condition. START and STOP conditions are always generated by the master. The I^{2}C bus is considered to be busy after the START condition. The I^{2}C bus is considered to be free again a certain time after the STOP condition.

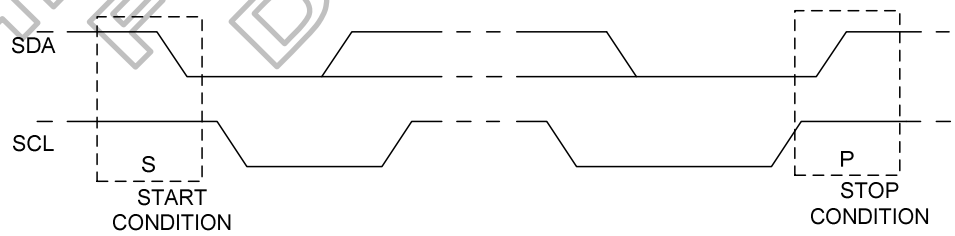

Figure 11.2: I^{2}C START/STOP

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The HX8571-E27ADBG-X only supports I^2C interface and the slave address is 90/94h. In I^2C slave mode, HX8571-E27ADBG-X waits for Master reading the data and acknowledges. Every byte put on the SDA line must be 8-bits long. The number of bytes that can be transmitted per transfer is unrestricted. Each byte has to be followed by an acknowledge bit. Data is transferred with the most significant bit **(MSB)** first.

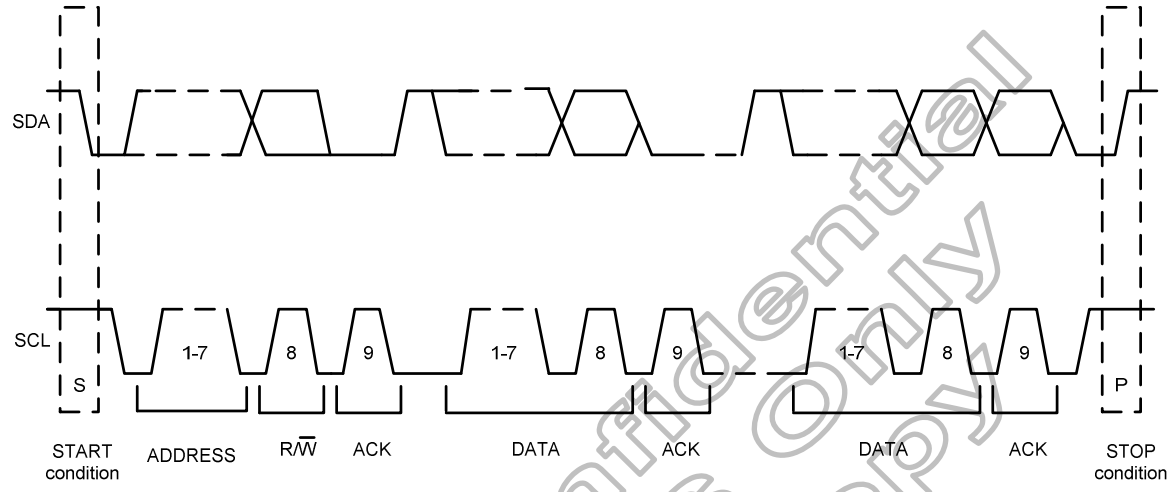

Figure 11.3: I^2C data transfer

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11.2.1 Format of data frame (I2C interface)

When master sends the command which be received by HX8571-E27ADBG-X, the HX8571-E27ADBG-X will responses the code and data. The format of communication is shown as Figure 11.4. The Command table that is written by master is defined on 14.1 Command list. HX8571-E27ADBG-X wills response the response code first and data later.

Write mode

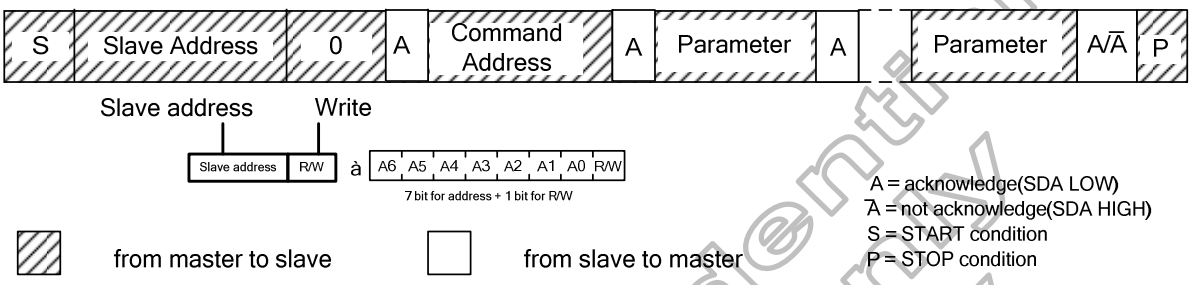

Figure 11.4: Data format of writing mode

Read mode

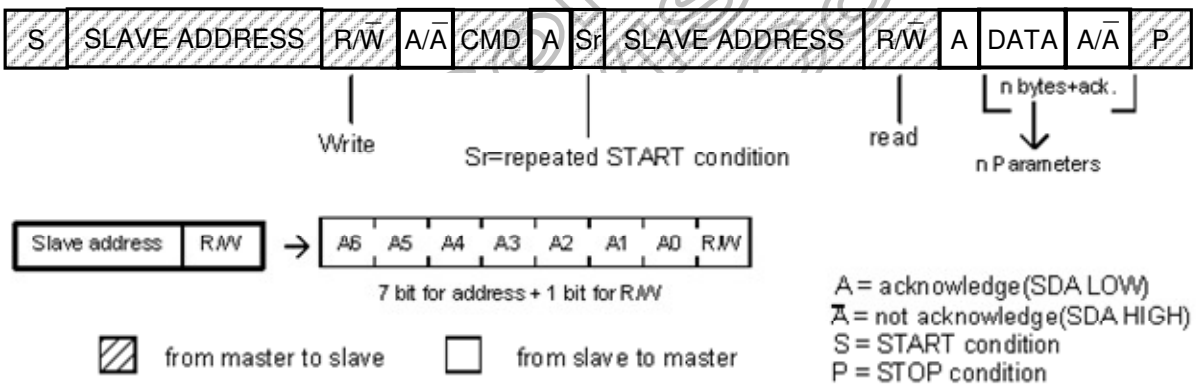

Figure 11.5: Data format of reading mode

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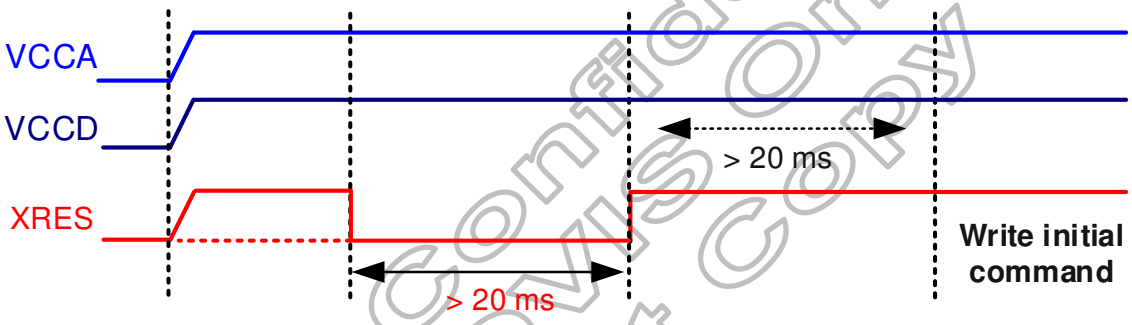
12. Hardware Power on Sequence

This part introduces the hardware power on sequence. The initial steps of power on HX8571-E27 touch controller would be specified as follows:

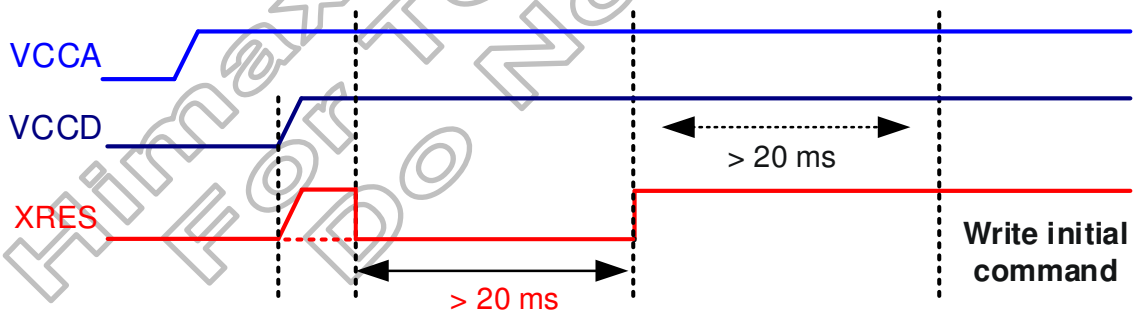
A. Input VCCA and VCCD power first (Both VCCA and VCCD can be pulled up first). VCCA range will be 2.7~3.6V. VCCD range will be 1.65~3.6V.
B. XRES PIN could pull high with VCCD at the same time or XRES keep at low level
C. When VCCA and VCCD voltage are stable, pull the XRES pin to low level or keep low level at least 20 ms.
D. After a delay of 20 ms, power on sequence has finished and need to write initial commands and settings.

Power On sequence with HW RST

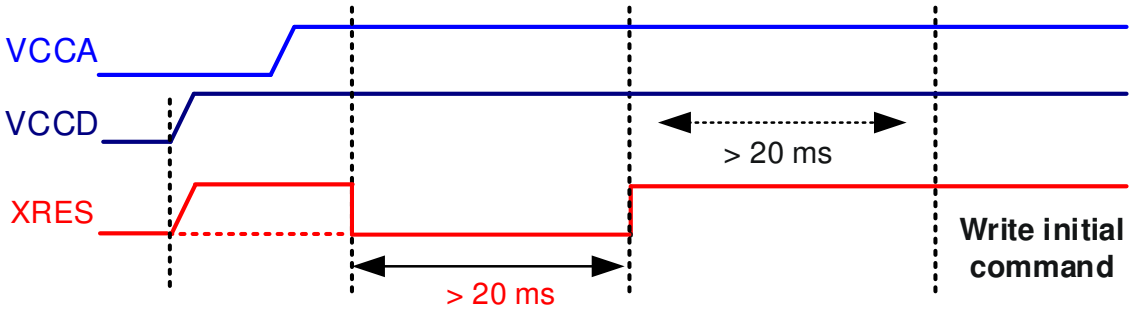
1. VCCA and VCCD power on simultaneously



2. VCCA power on firstly, and then VCCD power on



3. VCCD power on firstly, and then VCCA power on



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Power On sequence without HW RST

4. VCCA power on firstly, and then VCCD power on

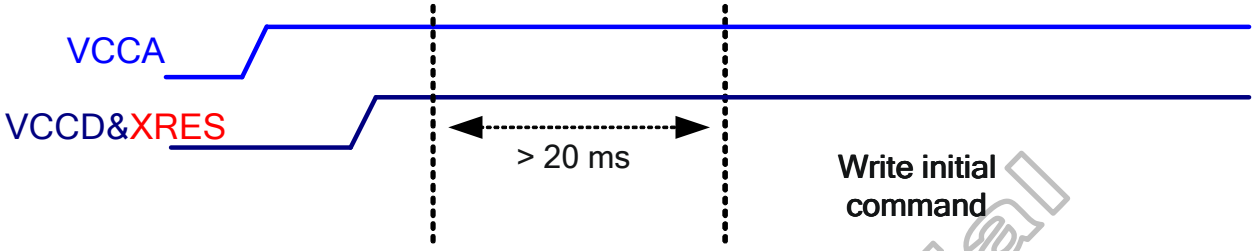

Figure 12.1: Power on sequence timing

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13. Flash Programming

13.1 Flash programming flow

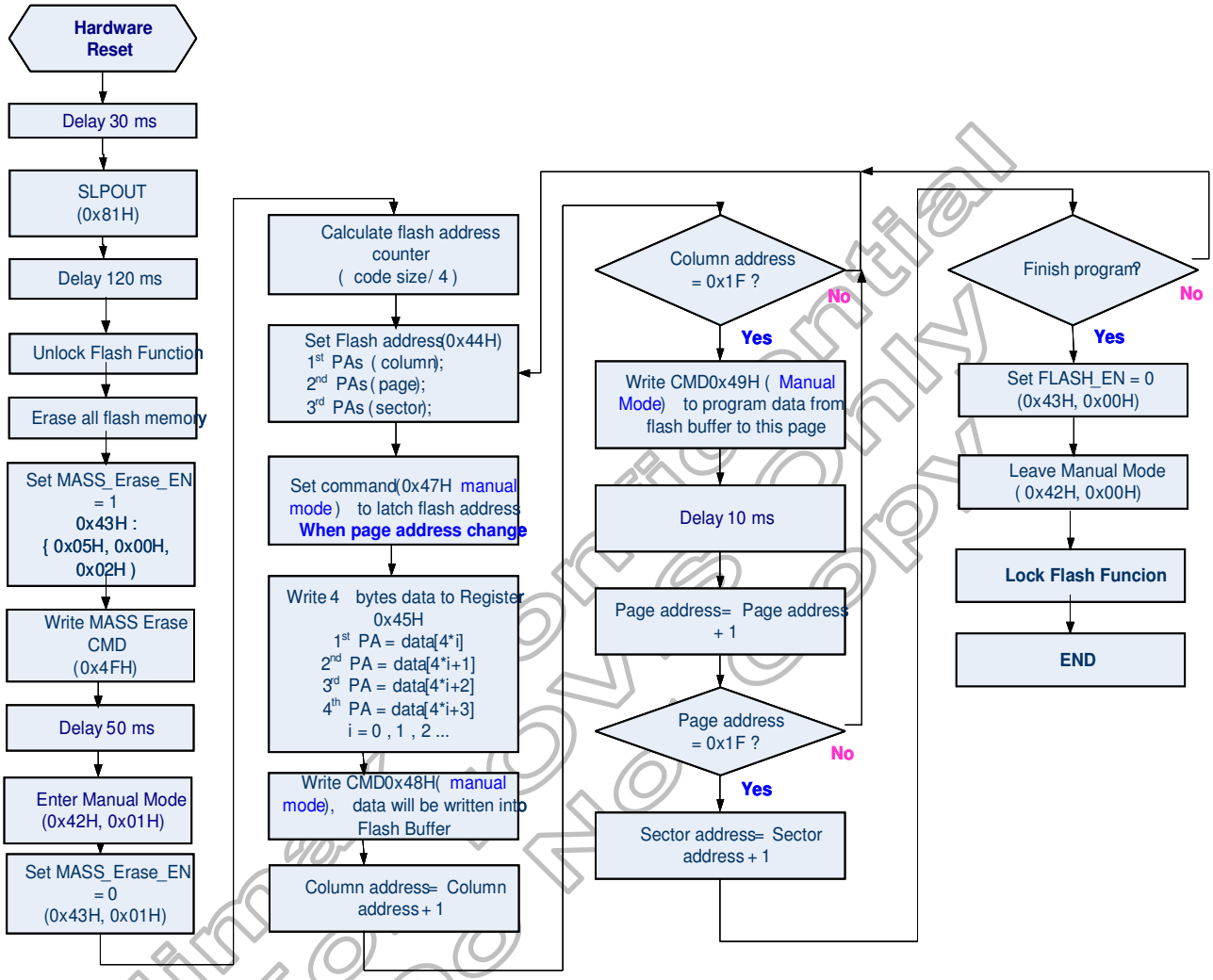

Figure 13.1: Flash programming flow

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14. Command

14.1 Command list

14.1.1 Standard command

(Hex)	Operation Code	D7	D6	D5	D4	D3	D2	D1	D0	Function
0	No operation	0	0	0	0	0	0	0	0	-
80	Sleep in	1	0	0	0	0	0	0	0	-
81	Sleep out	1	0	0	0	0	0	0	1	-
82	Sense off	1	0	0	0	0	0	1	0	-
83	Sense on	1	0	0	0	0	0	1	1	-
85	Read event	1	0	0	0	0	1	0	1	-
	1st parameter	B31	B30	B29	B28	B27	B26	B25	B24	-
	2nd parameter	B23	B22	B21	B20	B19	B18	B17	B16	-
	3rd parameter	B15	B14	B13	B12	B11	B10	B9	B8	-
	4th parameter	B7	B6	B5	B4	B3	B2	B1	B0	-
86	Read all events	1	0	0	0	0	1	1	0	-
	1st parameter	B31	B30	B29	B28	B27	B26	B25	B24	-
	2nd parameter	B23	B22	B21	B20	B19	B18	B17	B16	-
	3rd parameter	B15	B14	B13	B12	B11	B10	B9	B8	-
	4th parameter	B7	B6	B5	B4	B3	B2	B1	B0	-
	5th parameter	E3	E2	E1	E0	FI	P2	P1	P0	-
	6th parameter	B23	B22	B21	B20	B19	B18	B17	B16	-
	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮
	(n+1)th parameter	B7	B6	B5	B4	B3	B2	B1	B0	-
87	Read latest event	1	0	0	0	0	1	1	1	-
	1st parameter	B31	B30	B29	B28	B27	B26	B25	B24	-
	2nd parameter	B23	B22	B21	B20	B19	B18	B17	B16	-
	3rd parameter	B15	B14	B13	B12	B11	B10	B9	B8	-
	4th parameter	B7	B6	B5	B4	B3	B2	B1	B0	-
88	Clear Stack	1	0	0	0	1	0	0	0	-

14.1.2 User define command list table

CMD (Hex)	Operation code	D7	D6	D5	D4	D3	D2	D1	D0	Function
D1h	SETID1	1	1	0	1	0	0	0	1	SETID1
	1st parameter	ID1[7:0] **(8'h00)**								-
	2nd parameter	1'b1	ID2[6:0]							-
	3rd parameter	ID3[7:0] **(8'h00)**								-

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14.2 Command description

14.2.1 NOP

<table>
<tr><th rowspan="2">00 H</th><th colspan="10">NOP (No Operation)</th></tr>
<tr><th>DNC</th><th>D7</th><th>D6</th><th>D5</th><th>D4</th><th>D3</th><th>D2</th><th>D1</th><th>D0</th><th>HEX</th></tr>
<tr><td>Command</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>00</td></tr>
<tr><td>Parameter</td><td colspan="10">No parameter.</td></tr>
<tr><td>Description</td><td colspan="10">This command is an empty command and it does not have any effect on the touch screen.</td></tr>
<tr><td>Restriction</td><td colspan="10">-</td></tr>
<tr><td>Register Availability</td><td colspan="10"><table><tr><th>Status</th><th>Availability</th></tr><tr><td>TS Sleep Out</td><td>Yes</td></tr><tr><td>TS Sleep In</td><td>Yes</td></tr></table></td></tr>
<tr><td>Default</td><td colspan="10"><table><tr><th>Status</th><th>Default value</th></tr><tr><td>Power Up Sequence</td><td>N/A</td></tr><tr><td>TS S/W Reset</td><td>N/A</td></tr><tr><td>H/W Reset</td><td>N/A</td></tr></table></td></tr>
<tr><td>Flow Chart</td><td colspan="10">-</td></tr>
</table>

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14.2.2 TS sleep in (80h)

80 H	TSSLPIN (Touch Screen Sleep In)									
	DNC	D7	D6	D5	D4	D3	D2	D1	D0	HEX
Command	0	1	0	0	0	0	0	0	0	80
Parameter	No parameter									
Description	This command causes the touch screen to enter the minimum power consumption mode. MCU interface are register are still working and keeps their contents.									
Restriction	This command has no effect when the touch screen is already in TS Sleep In mode. TS Sleep In Mode can only be left by the TS Sleep Out Command **(81h)**. It will be necessary to wait 5msec before sending next command. This is to allow time for the supply voltages and clock circuits to stabilize. It will be necessary to wait 5msec after sending TS Sleep Out command **(when in TS Sleep In Mode)** before TS Sleep In command can be sent.									

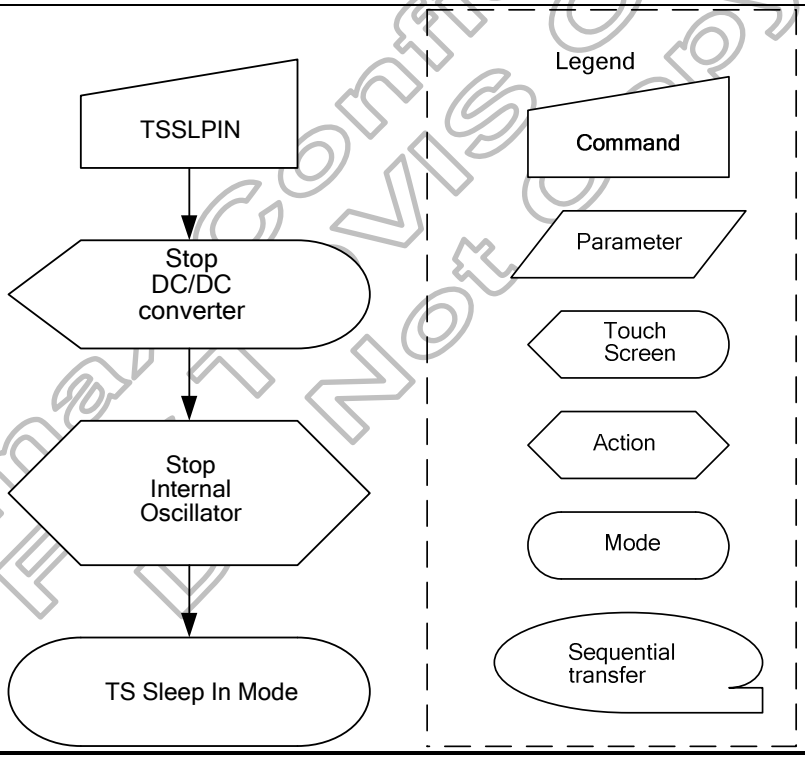
Register Availability

Status	Availability
TS Sleep Out	Yes
TS Sleep In	Yes

Default

Status	Default value
Power Up Sequence	TS Sleep In Mode
TS S/W Reset	TS Sleep In Mode
H/W Reset	TS Sleep In Mode

Flow Chart



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14.2.3 TS sleep out (81h)

81 H	TSSLPOUT (Touch Screen Sleep Out)									
	DNC	D7	D6	D5	D4	D3	D2	D1	D0	HEX
Command	0	1	0	0	0	0	0	0	1	81
Parameter	No parameter.									
Description	This command turns off TS Sleep In mode.									
Restriction	This command has no effect when touch screen is already in TS Sleep Out mode. TS Sleep Out Mode can only be left by the TS Sleep In Command **(80h)**. It will be necessary to wait 5msec before sending next command. This is to allow time for the supply voltages and clock circuits to stabilize. The touch screen loads all touch screen supplier's factory default values to the registers during this 5msec and there cannot be any abnormal effect on the touch screen functionality if factory default and register values are same when this load is done and when the touch screen is already TS Sleep Out – mode. It will be necessary to wait 5msec after sending TS Sleep In command **(when in TS Sleep Out mode)** before TS Sleep Out command can be sent.									

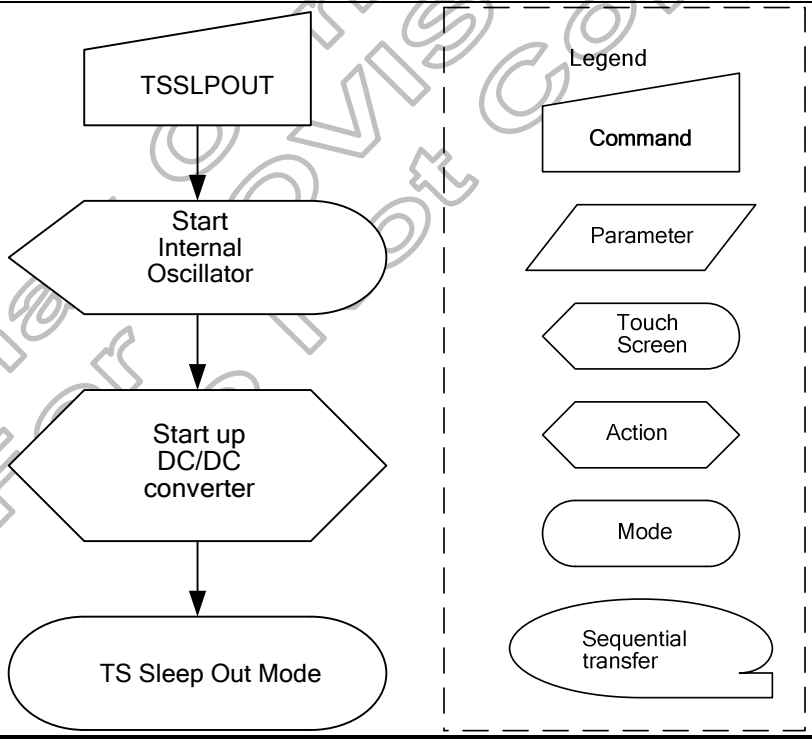
Register Availability

Status	Availability
TS Sleep Out	Yes
TS Sleep In	Yes

Default

Status	Default value
Power Up Sequence	TS Sleep In Mode
TS S/W Reset	TS Sleep In Mode
H/W Reset	TS Sleep In Mode

Flow Chart



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14.2.4 TS sense off (82h)

82 H	TSSOFF (Touch Screen Sense Off)									
	DNC	D7	D6	D5	D4	D3	D2	D1	D0	HEX
Command	0	1	0	0	0	0	0	1	0	82
Parameter	No parameter.									
Description	The touch screen is not sensing touches **(= No new events)**, but the touch screen is still scanning.									
Restriction	-									

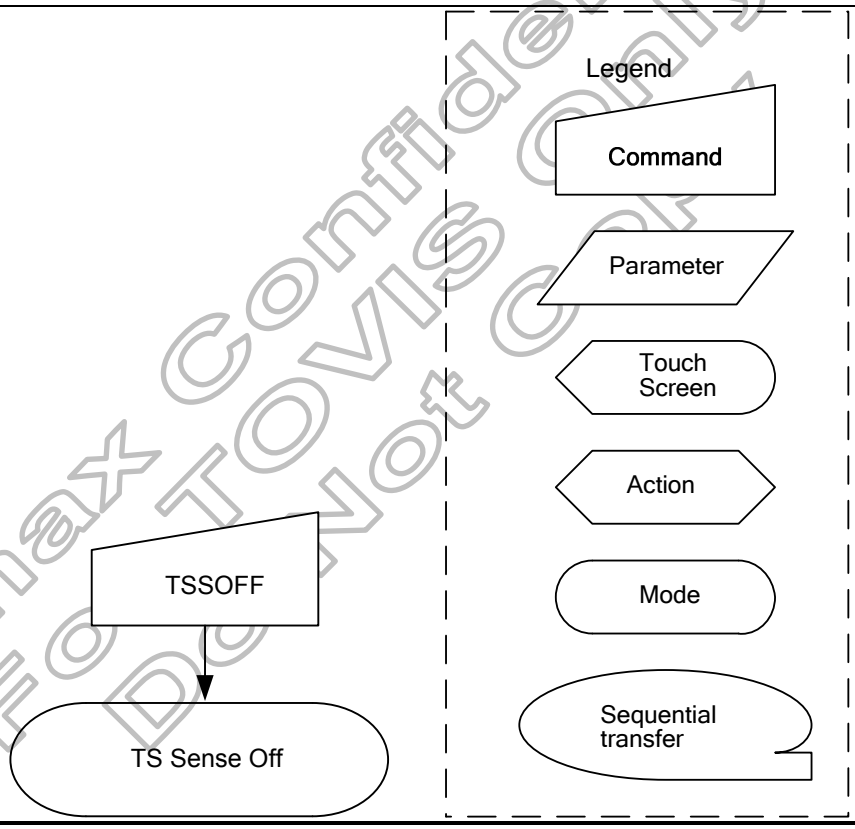
Register Availability

Status	Availability
TS Sleep Out	Yes
TS Sleep In	Yes

Default

Status	Default value
Power Up Sequence	TS Sense Off
TS S/W Reset	TS Sense Off
H/W Reset	TS Sense Off

Flow Chart



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14.2.5 TS sense on (83h)

83 H	TSSON (Touch Screen Sense On)									
	DNC	D7	D6	D5	D4	D3	D2	D1	D0	HEX
Command	0	1	0	0	0	0	0	1	1	83
Parameter	No parameter.									
Description	The touch screen is sensing touches **(= No new events)**.									
Restriction	-									

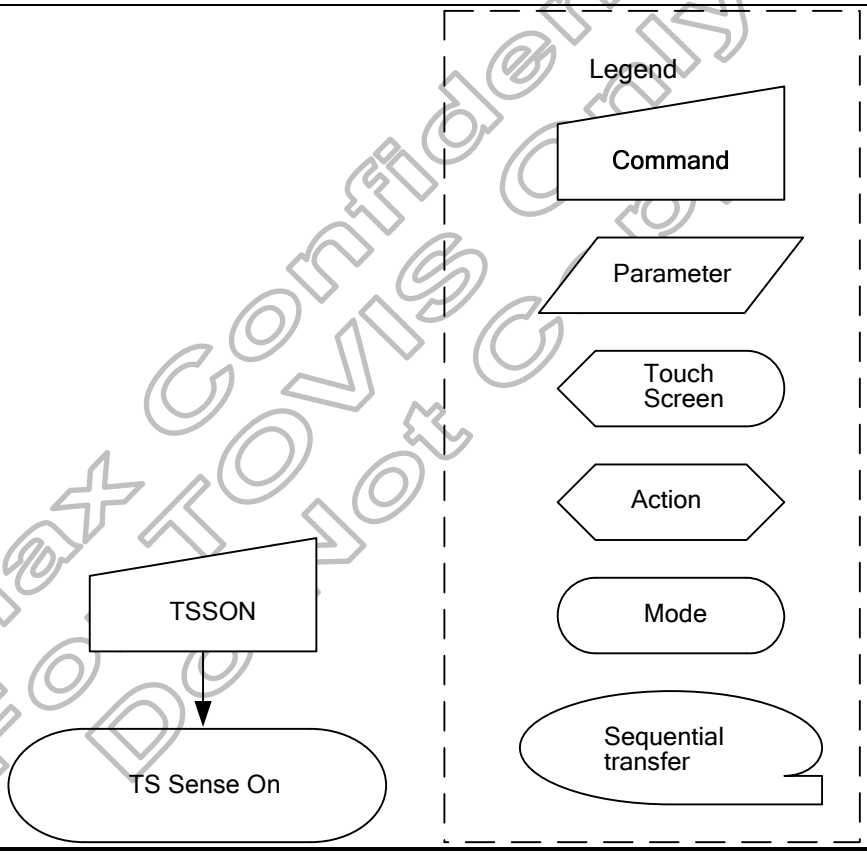
Register Availability

Status	Availability
TS Sleep Out	Yes
TS Sleep In	Yes

Default

Status	Default value
Power Up Sequence	TS Sense Off
TS S/W Reset	TS Sense Off
H/W Reset	TS Sense Off

Flow Chart



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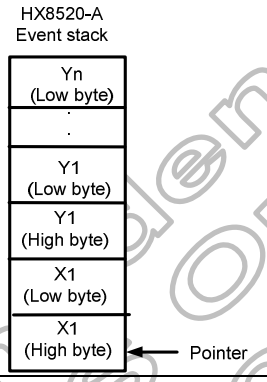
14.2.6 Read one event (85h)

85 H	ROE (Read One Event)									
	DNC	D7	D6	D5	D4	D3	D2	D1	D0	HEX
Command	0	1	0	0	0	0	1	0	1	85
1st parameter	-	B31	B30	B29	B28	B27	B26	B25	B24	xx
2nd parameter	-	B23	B22	B21	B20	B19	B18	B17	B16	xx
3rd parameter	-	B15	B14	B13	B12	B11	B10	B9	B8	xx
4th parameter	-	B7	B6	B5	B4	B3	B2	B1	B0	xx

Description

This command returns touch co-ordinates what is the oldest co-ordinates information what has been stored on the stock. A returning value can be "No Event" if the stock is empty. The default assignment is list as below. The assignment of event stack also can be modified if necessary **(base on the requirement of customer)**.



Restriction

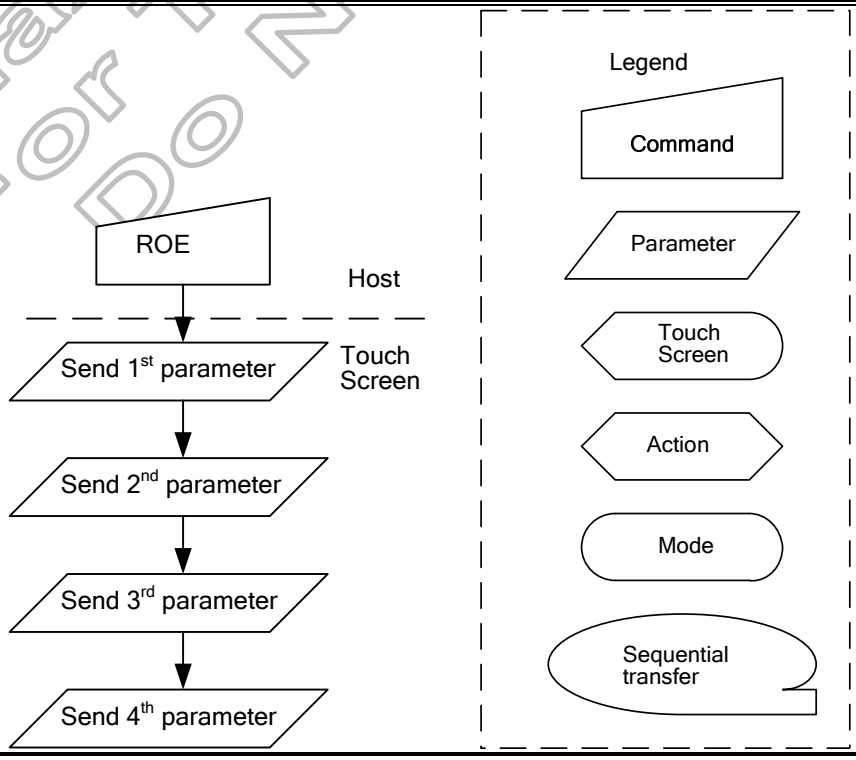
Register Availability

Status	Availability
TS Sleep Out	Yes
TS Sleep In	Yes

Default

Status	Default value
Power Up Sequence	0000 0000h
TS S/W Reset	0000 0000h
H/W Reset	0000 0000h

Flow Chart



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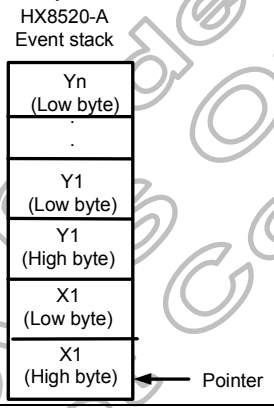
14.2.7 Read all event (86h)

86 H	RAE (Read All Events)									
	DNC	D7	D6	D5	D4	D3	D2	D1	D0	HEX
Command	0	1	0	0	0	0	1	1	0	86
1st parameter	-	B7	B6	B5	B4	B3	B2	B1	B0	xx
2nd parameter	-	B15	B14	B13	B12	B11	B10	B9	B8	xx
3rd parameter	-	B23	B22	B21	B20	B19	B18	B17	B16	xx
4th parameter	-	B31	B30	B29	B28	B27	B26	B25	B24	xx
5th parameter	-	B39	B38	B37	B36	B35	B34	B33	B32	xx
6th parameter	-	B47	B46	B45	B44	B43	B42	B41	B40	xx
:	-	:	:	:	:	:	:	:	:	:
(n+1)th parameter	-	B(N)	B6 (n-1)	B6 (n-2)	B6 (n-3)	B6 (n-4)	B6 (n-5)	B6 (n-6)	B6 (n-7)	xx

Description

This command returns touch co-ordinates what is the oldest co-ordinates information what has been stored on the stock. A returning value can be "No Event" if the stock is empty. The default assignment is list as below. The assignment of event stack also can be modified if necessary **(base on the requirement of customer)**.



Restriction

This read command cannot use with LoSSI.

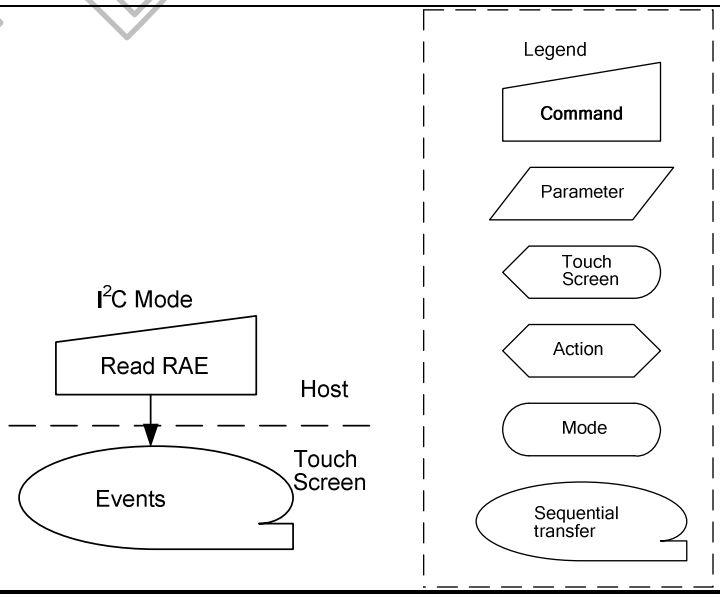
Register Availability

Status	Availability
TS Sleep Out	Yes
TS Sleep In	Yes

Default

Status	Default value
Power Up Sequence	All Values 0000 0000h
TS S/W Reset	All Values 0000 0000h
H/W Reset	All Values 0000 0000h

Flow Chart



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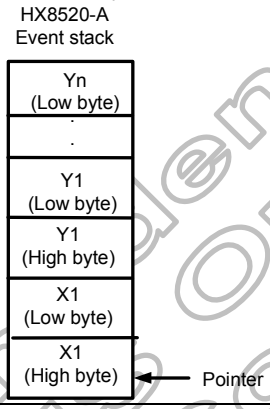
14.2.8 Read latest event (87h)

87H	RLE (Read Latest Event)									
	DNC	D7	D6	D5	D4	D3	D2	D1	D0	HEX
Command	0	1	0	0	0	0	1	1	1	87
1st parameter	-	B31	B30	B29	B28	B27	B26	B25	B24	xx
2nd parameter	-	B23	B22	B21	B20	B19	B18	B17	B16	xx
3rd parameter	-	B15	B14	B13	B12	B11	B10	B9	B8	xx
4th parameter	-	B7	B6	B5	B4	B3	B2	B1	B0	xx

Description

This command returns one touch event what is the latest co-ordinates information what has been stored on the stock. The event stack is empty after this command. A returning value can be “No Event” if the stock is empty. The default assignment is list as below. The assignment of event stack also can be modified if necessary **(base on the requirement of customer)**.



Restriction

-

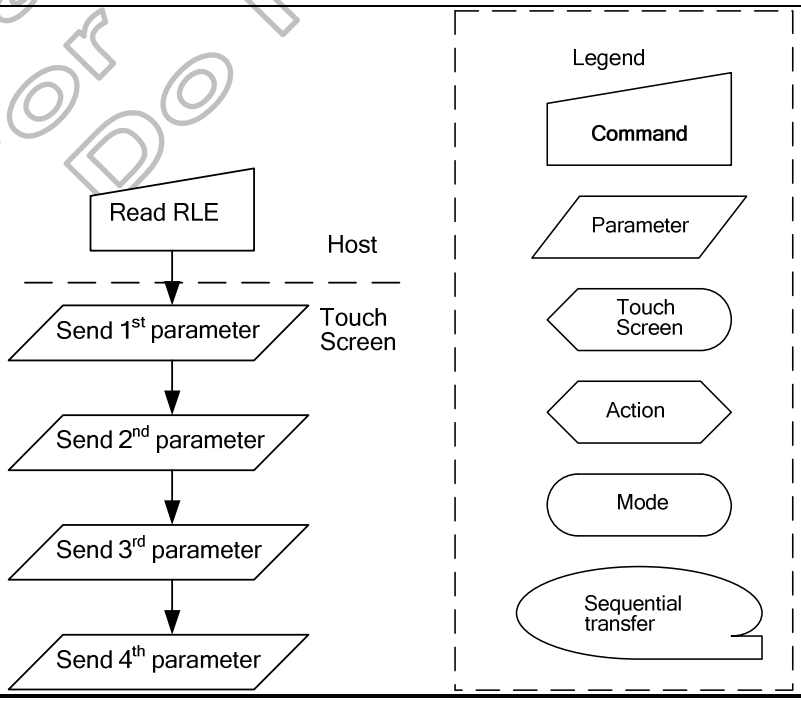
Register Availability

Status	Availability
TS Sleep Out	Yes
TS Sleep In	Yes

Default

Status	Default value
Power Up Sequence	0000 0000h
TS S/W Reset	0000 0000h
H/W Reset	0000 0000h

Flow Chart



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14.2.9 Clear event stack (88h)

88 H	CLRES (Clear Event Stack)									
	DNC	D7	D6	D5	D4	D3	D2	D1	D0	HEX
Command	0	1	0	0	0	1	0	0	0	88
Parameter	No parameter.									
Description	This command clears event stack when the only return event can be “No Event”.									
Restriction	-									

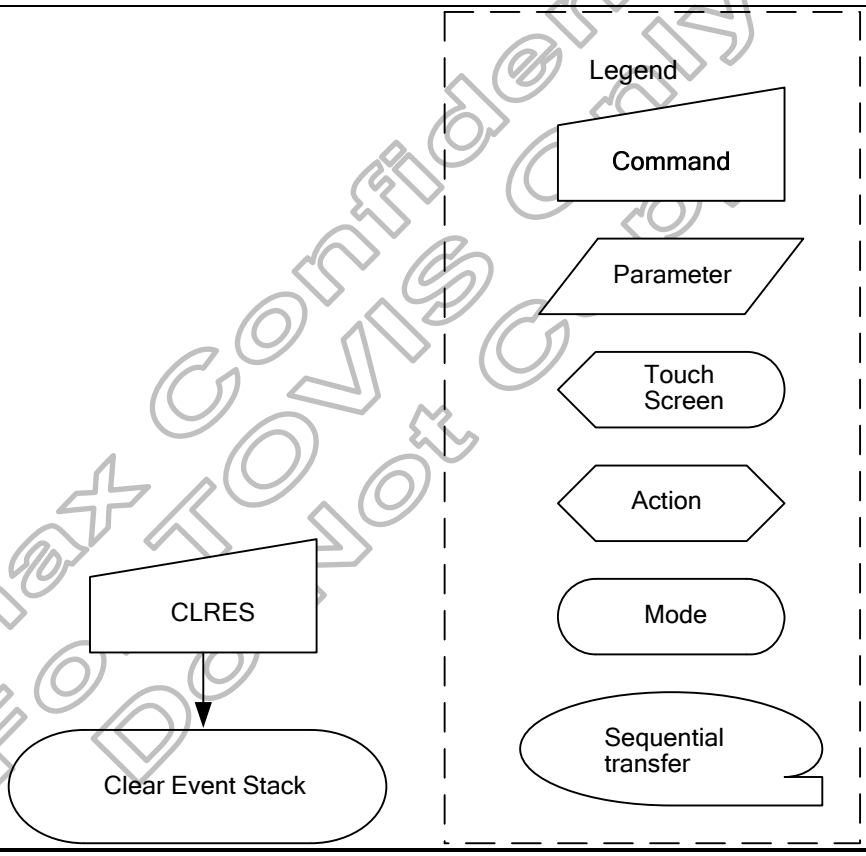
Register Availability

Status	Availability
TS Sleep Out	Yes
TS Sleep In	Yes

Default

Status	Default value
Power Up Sequence	Empty Stack
TS S/W Reset	Empty Stack
H/W Reset	Empty Stack

Flow Chart



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14.2.10 IC part number (D1h)

D1H	ID1 (IC Part Number)								
	D7	D6	D5	D4	D3	D2	D1	D0	HEX
Command	1	1	0	1	0	0	0	1	D1
1st parameter	05								00..FF
2nd parameter	85								00..FF
3rd parameter	71								00..FF
Description	When the Device ID command is written, HX8571-E27ADBG-X will echo the device ID to master. The index of IC part number command is D1h								
Register Availability									

Status	Availability
TS Sleep Out	Yes
TS Sleep In	Yes

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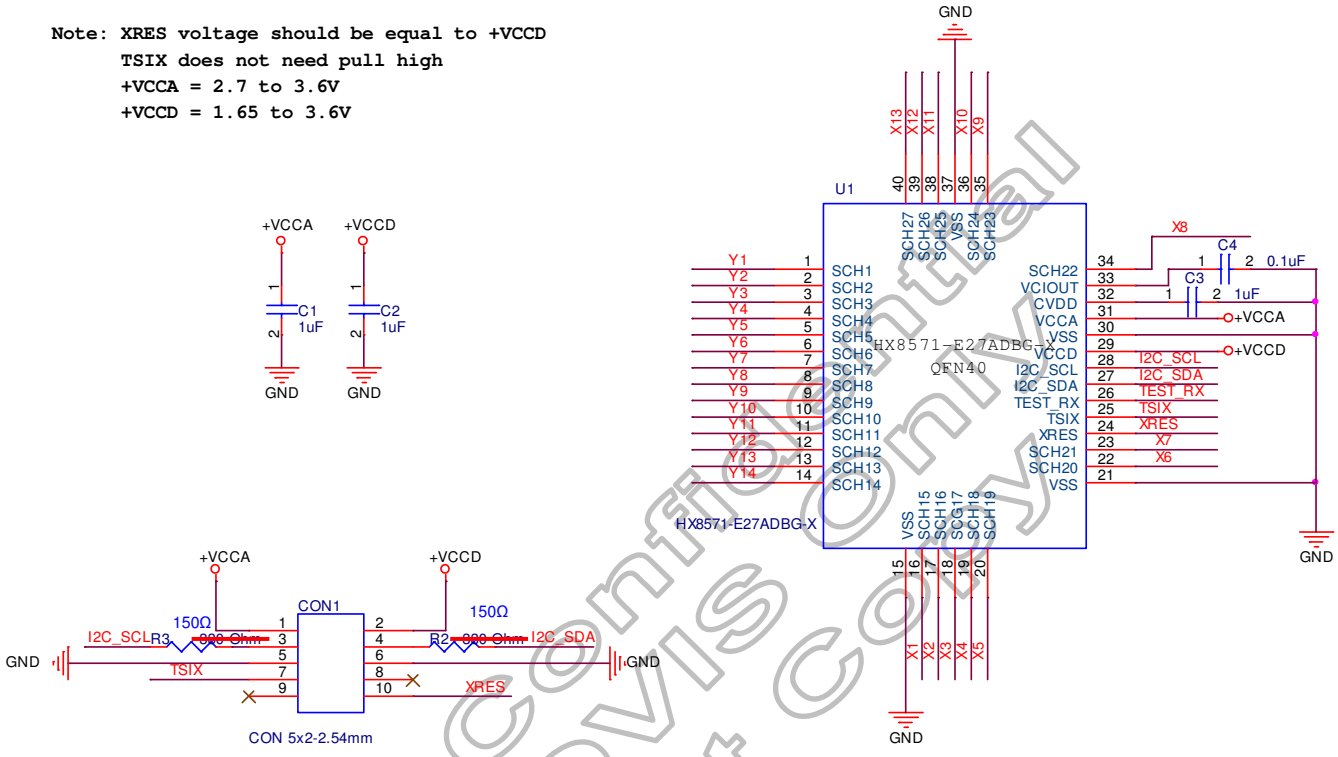
15. Reference circuit

15.1 HX8571-E27ADBG-X

Note: XRES voltage should be equal to +VCCD
TSIX does not need pull high
+VCCA = 2.7 to 3.6V
+VCCD = 1.65 to 3.6V



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16. Ordering Information

Part no.	Package type
HX8571-E27ADBG-X	QFN 40 **(3mmX5mmX0.55mm)**

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