

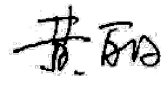
PRODUCT SPECIFICATION

CDTECH Model: **S080QSV26EA-DC27**

CUSTOMER Model: **-**

Description: **8.0 " TFT-LCD Module with CTP**

Version: **1.0**

CDTECH	PREPARED BY	CHECKED BY	APPROVED BY
SIGNATURE			
DATE	2023.8.10	2023.8.10	2023.8.10

CUSTOMER APPROVAL	SIGNATURE	DATE



Record of Revisions

[illegible]



Contents

1. General Specifications	4
2. Absolute Maximum Ratings	5
3. Electrical Characteristics	5
4. Interface Pin Assignment	7
5. Interface Characteristics	9
6. Optical Specifications	11
7. Reliability Test Items	14
8. Mechanical Drawing	15
9. Packing	16
10. Precautions for Use of LCD modules	17

1. General Specifications

1.1 LCM General Information

Item	Specification	Unit
LCD Size	8.0	inch
Number of Pixels	800 (H) RGB x 600 (V)	pixels
Display Mode	Normally White	-
Viewing Direction	12 o' clock	o' clock
Interface	RGB	-
Display Colors	16.7M	colors
Outline Dimension	203.30 (H) x 165.20 (V) x 8.12 (D)	mm
Active Area	162.00 (H) x 121.50 (V)	mm
Pixel Pitch	0.2025 (H) x 0.2025 (V)	mm
Driver IC	EK79713CA+EK73215BCGA	-
Operation Temperature	-20~70	°C
Storage Temperature	-30~80	°C

1.2 Touch Panel Information

Item	Specification
Touch Structure	G+G
Bonding Type with LCM	Perimeter Bonding
Driver IC	ILI2131
Interface	I ² C
Touch Count Max	5 Points
Surface treatment	-
Surface hardness	6H
I2C slave address	0x82
Origin of coordinate	Top Left Corner

Note1:Requirements on environmental protection RoHS compliant.

2. Absolute Maximum Ratings

Item	Symbol	MIN.	MAX.	Unit	Note
Analog Supply voltage	VCC	-0.3	5.0	V	Note 1

Note 1: Permanent damage may occur to the LCD module if beyond this specification.

Functional operation should be restricted to the conditions described under normal operating conditions.

3. Electrical Characteristics

3.1 Recommended Operating Condition for TFT LCD

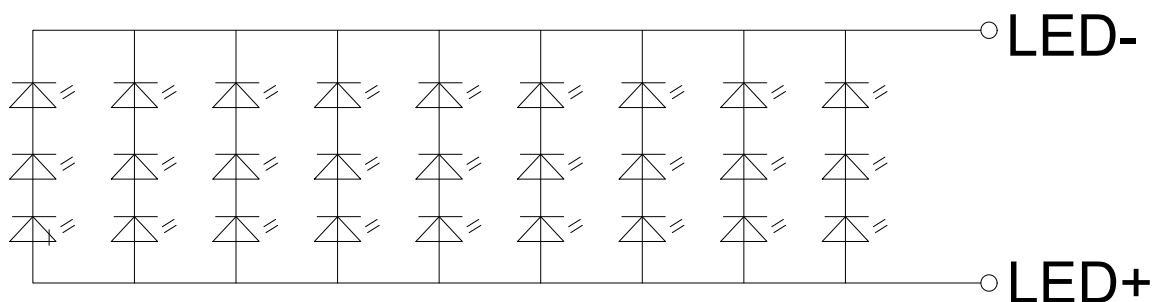
Item	Symbol	Min.	Typ.	Max.	Unit	Note
Analog Supply voltage	VCC	3.0	3.3	3.6	V	
Analog supply current	I _{VCC}	-	TBD	-	mA	VCC=3.3V
Power supply for LCD	AVDD	9.0	9.8	10.5	V	
	VGH	15.5	16.0	16.5	V	
	VGL	-7.5	-7.0	-6.5	V	
	VCOM	3.5	4.0	4.5	V	
Logic input voltage	VIH	0.7*VCC	-	VCC	V	
	VIL	GND	-	0.3*VCC	V	

3.2 Recommended Driving Condition for Backlight

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Driving Current	I _F	-	540	-	mA	
Driving Voltage	V _F	8.1	-	10.2	V	
Power consumption	W _{BL}	4.374	-	5.508	W	
LED Life-Time	N/A	-	50,000	-	Hours	Ta=25°C Note 1

Note 1: LED lifetime is defined as the module brightness decay 50% of original brightness at Ta=25 degree, typical current.

Note 2:LED circuit :



3.3 Touch Panel

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Power Supply voltage	VDD	-	3.3	-	V	
Analog supply current	I _{VDD}	-	TBD	-	mA	VDD=3.3V
Input high-level voltage	VIH	0.7*VDD	-	VDD	V	
Input low -level voltage	VIL	GND	-	0.3*VDD	V	

4. Interface Pin Assignment

4.1 LCM Pin Assignment

No.	Symbol	Description
1-4	NC	No connection
5	GND	Ground
6	VCOM	Common Voltage
7	VCC	Power supply
8	MODE	DE / SYNC mode select under TTL mode. Normally pull high H : DE mode L : HSD/VSD mode
9	DE	Data enable input. Active high to enable the input data bus under “DE Mode”
10	VS	Vertical sync input. Negative polarity
11	HS	Horizontal sync input. Negative polarity
12-19	B7-B0	Data bus
20-27	G7-G0	Data bus
28-35	R7-R0	Data bus
36	GND	Ground
37	DOCLK	Dot-clock signal and oscillator source
38	GND	Ground
39	L/R	Horizontal inversion, Normally pull high
40	U/D	Vertical inversion, Normally pull low
41	VGH	Gate on Voltage
42	VGL	Gate off Voltage
43	AVDD	Power for Analog Circuit
44	RESET	Global reset pin
45	NC	No connection
46	VCOM	Common Voltage
47	DITHB	Dithering function enable control, Normally pull high
48	GND	Ground
49	NC	No connection
50	NC	No connection

4.2 Touch FPC Pin Assignment

No.	Symbol	Description
1	VDD 3.3V	Power supply for CTP (3.3V)
2	INT 3.3V	Interrupt signal for CTP (3.3V)
3	SDA 3.3V	I2C data input and output for CTP (3.3V)
4	SCL 3.3V	I2C clock input for CTP (3.3V)
5	RST 3.3V	Reset Pin for CTP
6	GND	Ground

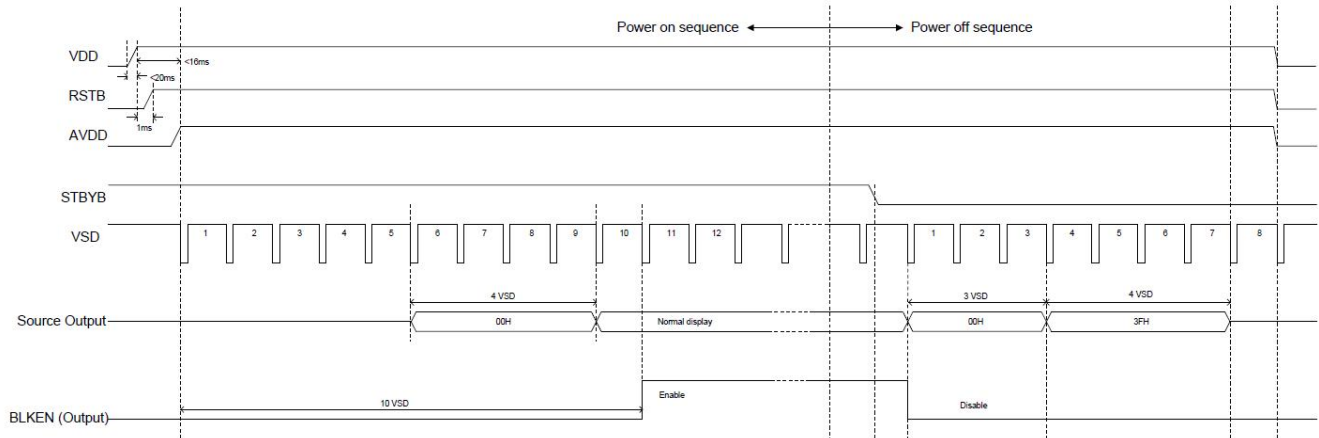
4.3 Backlight LED FPC Pin Assignment

No.	Symbol	Description
1	LED A	Power for LED backlight (Anode)
2	LED K	Power for LED backlight (Cathode)

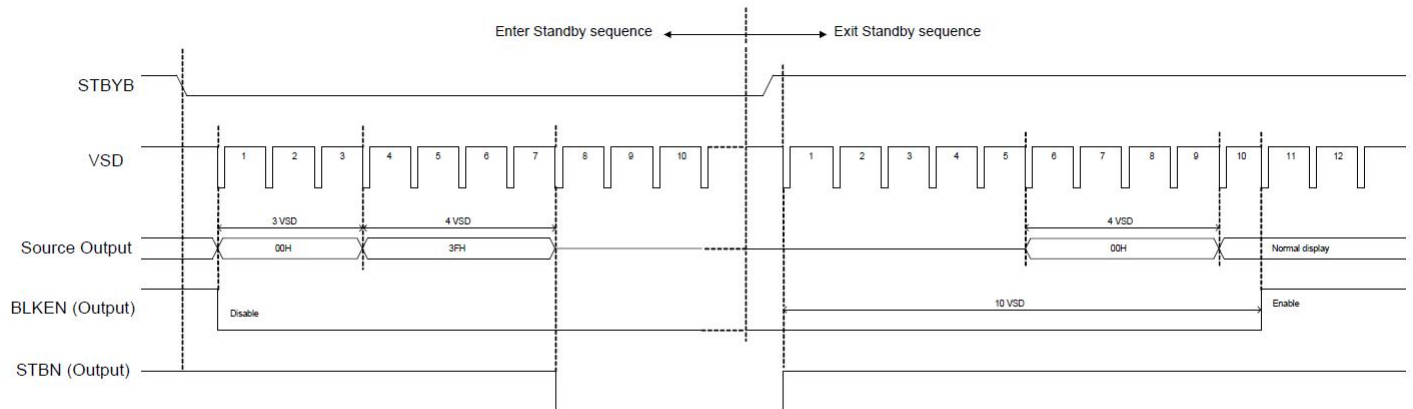
5. Interface Characteristics

5.1 Power On/Off Sequence

In order to prevent IC from power on reset fail, the rising time (TPOR) of the digital power supply VDD should be maintained within the given specifications. Refer to “AC Characteristics” for more detail on timing. This is another paragraph of sub-function description.



Power-On/Off Timing Sequence



Enter and Exit Standby Mode Sequence

5.2 Timing Characteristic

For 800 × 600 panel :

Horizontal input timing

Parameter		Symbol	Value			Unit
Horizontal display area		thd	800			DCLK
DCLK frequency		fclk	Min.	Typ.	Max	
			-	40	50	MHz
1 Horizontal Line		th	862	1056	1200	DCLK
HSD pulse width	Min.	thpw	1			
	Typ.		-			
	Max.		40			
HSD Back Porch (Blanking)		thb	46	46	46	
HSD Front Porch		thfp	16	210	354	

Vertical input timing

Parameter	Symbol	Min.	Typ.	Max.	Unit
Vertical display area	tv	600			H
VSD period time	tv	624	635	700	H
VSD pulse width	tvpw	1	-	20	H
VSD Back Porch (Blanking)	tvb	23	23	23	H
VSD Front Porch	tvfp	1	12	77	H

5.3 AC Characteristics

(TA = -20 to 85°C, VDD = 1.8 to 3.6V, AVDD = 6.5 to 13.5V, GND = AVSS = 0V)

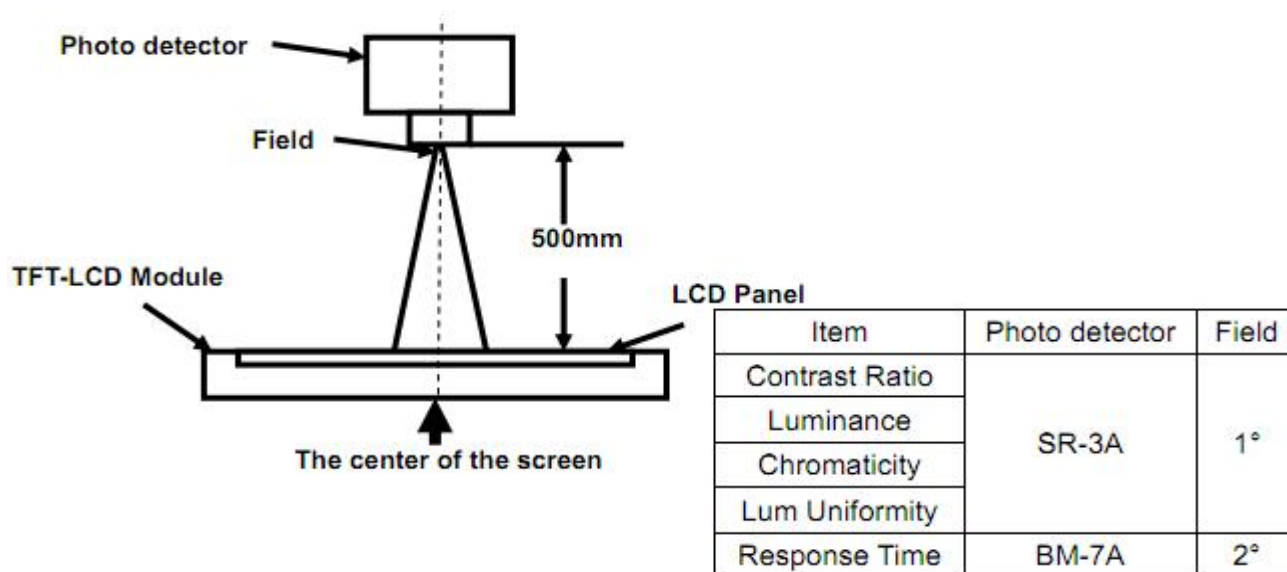
Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
VDD Power On Slew rate	TPOR	From 0V to 90% VDD	-	-	20	ms
RSTB pulse width	TRST	CLKIN = 40MHz	1	-	-	ms
CLKIN cycle time	Tcph	-	20	-	-	ns
CLKIN pulse duty	Tcwh	-	40	50	60	%
VSD setup time	Tvst	-	8	-	-	ns
VSD hold time	Tvhd	-	8	-	-	ns
HSD setup time	Thst	-	8	-	-	ns
HSD hold time	Thhd	-	8	-	-	ns
Data set-up time	Tdsu	D0[7:0], D1[7:0], D2[7:0] to CLKIN	8	-	-	ns
Data hold time	Tdhd	D0[7:0], D1[7:0], D2[7:0] to CLKIN	8	-	-	ns
DEN setup time	Tesu	-	8	-	-	ns
DEN hold time	Tehd	-	8	-	-	ns
Output stable time	Tsst	10% to 90% target voltage. CL=120pF, R=10K ohm	-	-	6	us

6. Optical Specifications

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Viewing Angle (CR≥10) B/L ON	θ_T	$\Phi=90^\circ$ (12 o'clock)	40	50	-	deg	Note2
	θ_B	$\Phi=270^\circ$ (6 o'clock)	60	70	-	deg	Note2
	θ_L	$\Phi=180^\circ$ (9 o'clock)	60	70	-	deg	Note2
	θ_R	$\Phi=0^\circ$ (3 o'clock)	60	70	-	deg	Note2
Response Time	T_{ON}	Normal $\theta=\Phi=0^\circ$	-	12	25	msec	Note4
	T_{OFF}		-	12	25	msec	Note4
Contrast Ratio	CR		800	1000	-	-	Note1 Note3
Color Chromaticity	W_X		0.257	0.307	0.357	-	Note1 Note5
	W_Y		0.290	0.340	0.390	-	Note1 Note5
Luminance	L		750	850	-	cd/m ²	Note1 Note7
Luminance Uniformity	Y_U		75	80	-	%	Note1 Note6
NTSC	-		-	51.45	-	%	-

Note 1:Definition of optical measurement system

The optical characteristics should be measured in dark room. After 5 minutes operation, the optical properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.



Note 2: Definition of viewing angle range and measurement system

Viewing angle is measured at the center point of the LCD by CONOSCOPE(ergo-80).

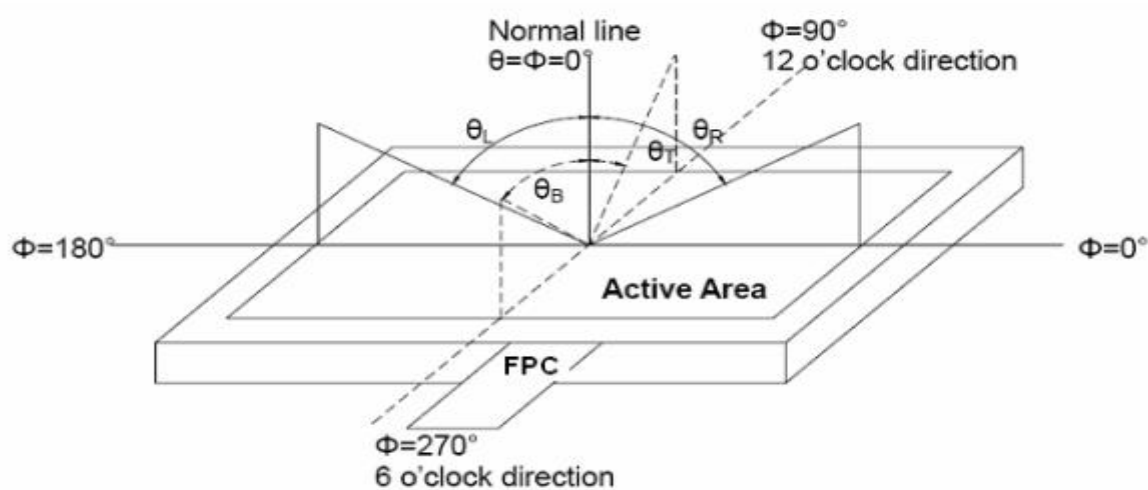


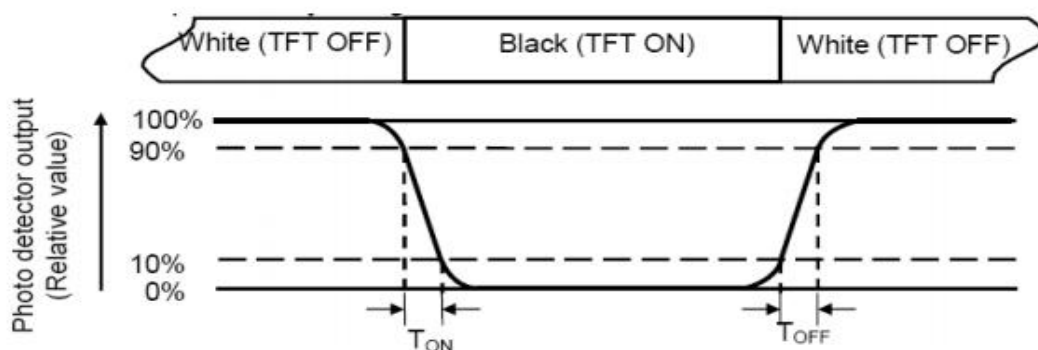
Fig. 1 Definition of viewing angle

Note 3: Definition of contrast ratio

$$CR = \frac{\text{Luminance with all pixels white}}{\text{Luminance with all pixels black}}$$

Note 4: Definition of Response time

The response time is defined as the LCD optical switching time interval between “White” state and “Black” state. Rise time (TON) is the time between photo detector output intensity changed from 90% to 10%. And fall time (TOFF) is the time between photo detector output intensity changed from 10% to 90%.



Note 5: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

Note 6: Definition of Luminance Uniformity

The luminance uniformity in surface luminance is determined by measuring luminance at each test position 1 through n, and then dividing the maximum luminance of n points luminance by minimum luminance of n points luminance. For more information see FIG.2.

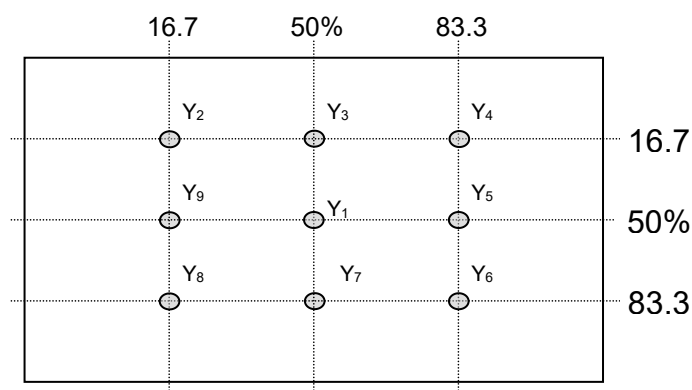


Fig. 2 Definition of points

Note 7: Definition of Luminance (Refer Fig. 2)

Surface luminance is the luminance with all pixels displaying white.

L_v = Average Surface Luminance with all white pixels($P_1, P_2, P_3, \dots, P_n$).

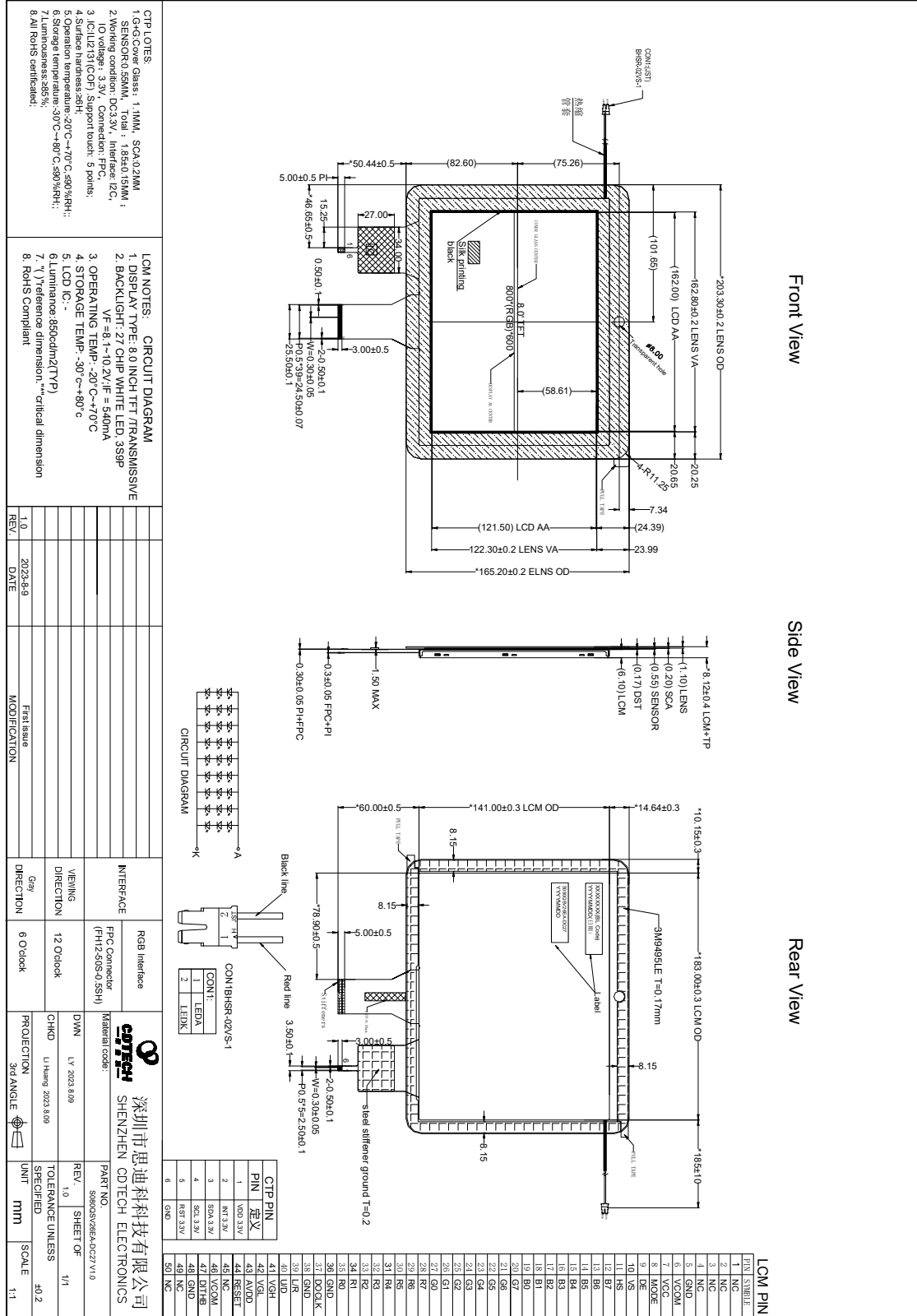
7. Reliability Test Items

Test Item	Test Conditions
High Temperature Storage	Ta= +80℃ 96hrs
Low Temperature Storage	Ta= -30℃ 96hrs
High Temperature Operation	Ta= +70℃ 96hrs
Low Temperature Operation	Ta= -20℃ 96hrs
High Temperature and Humidity Storage	Ta= +60℃, 90% RH 96hrs
Thermal Shock (Non-operation)	-30℃/30 min ~ +80℃/30 min for 20 cycles Start with cold temperature end with high temperature
Electro Static Discharge	Contact = ± 4 kV, class B Air = ± 8 kV, class B R=330Ω,C=150pF
Vibration	Sweep: 10Hz~55Hz~10Hz Stroke: 1.5mm 2 hrs for each direction of X .Y. Z.
Mechanical Shock	60G 6ms,±X,±Y,±Z 3 times for each direction
Package Drop Test	Height: 60 cm 1 corner, 3 edges, 6 surfaces

Notes: The test result shall be evaluated after the sample has been left at room temperature and humidity for 2 hours without load. No condensation shall be accepted. The sample will not be accepted if appear these defects:

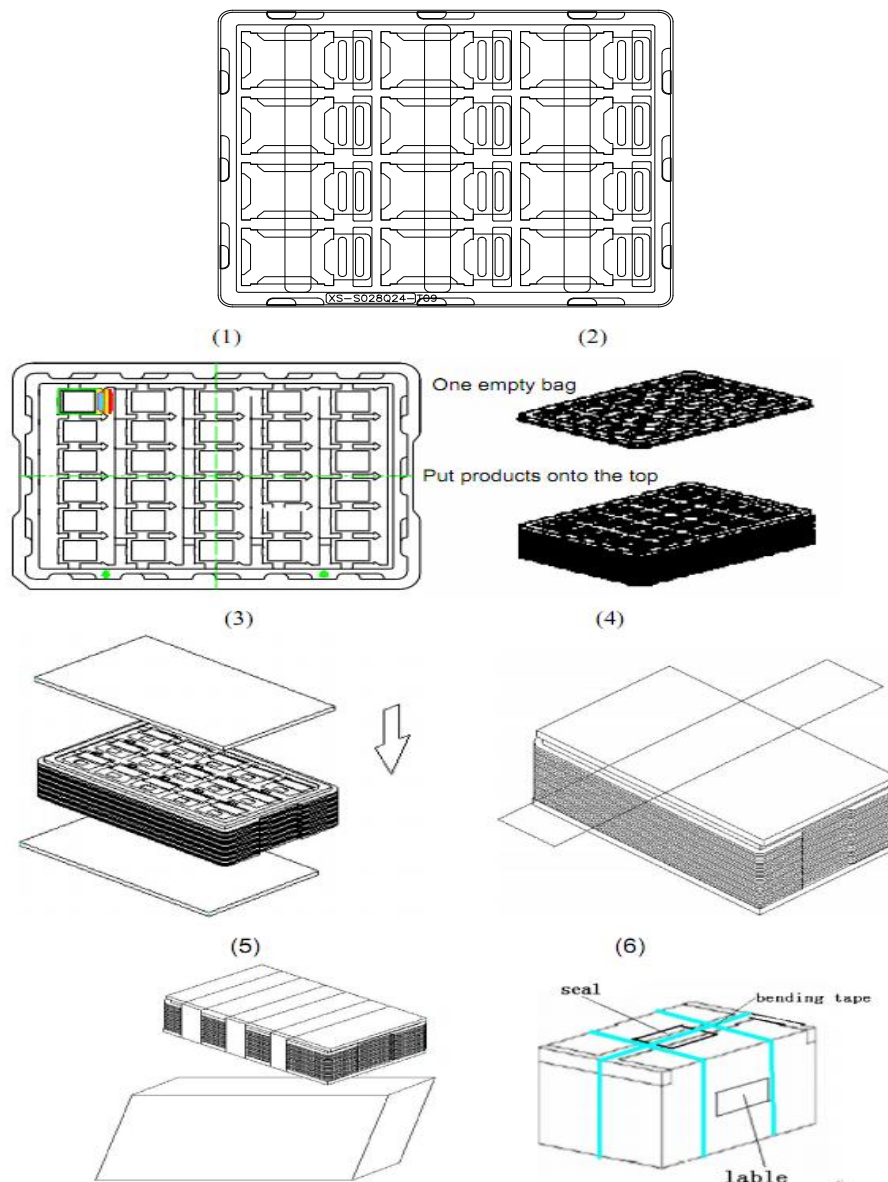
- 1). Air bubble in the LCD
- 2). Seal leak or Glass crack
- 3). Non display or abnormal display
- 4). Brightness reduction >50%

8. Mechanical Drawing



9. Packing

Packing Method



Steps:

1. Put module into tray cavity
2. Tray stacking
3. Put 1 cardboard under the tray stack and 1 cardboard above
4. Fix the cardboard to the tray stack with adhesive tape
5. Put the tray stack into carton
6. Carton sealing with adhesive tape

10. Precautions for Use of LCD modules

10.1 Handling Precautions

10.1.1. The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

10.1.2. If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

10.1.3. Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

10.1.4. The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

10.1.5. If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

- Isopropyl alcohol
- Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketene
- Aromatic solvents

10.1.6. Do not attempt to disassemble the LCD Module.

10.1.7. If the logic circuit power is off, do not apply the input signals.

10.1.8. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

10.1.8.1. Be sure to ground the body when handling the LCD Modules.

10.1.8.2. Tools required for assembly, such as soldering irons, must be properly ground.

10.1.8.3. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

10.1.8.4. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

10.2 Storage Precautions

10.2.1. When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

10.2.2. The LCD modules should be stored under the storage temperature range if the LCD modules will be stored for a long time, the recommend condition is :

Temperature : 0℃ ~40℃ Relatively humidity: ≤80%

10.2.3. The LCD modules should be stored in the room without acid, alkali and harmful gas.

10.3 Transportation Precautions

The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.